INGENIC

RD4750 APUS Board
Schematic Revision 1.2

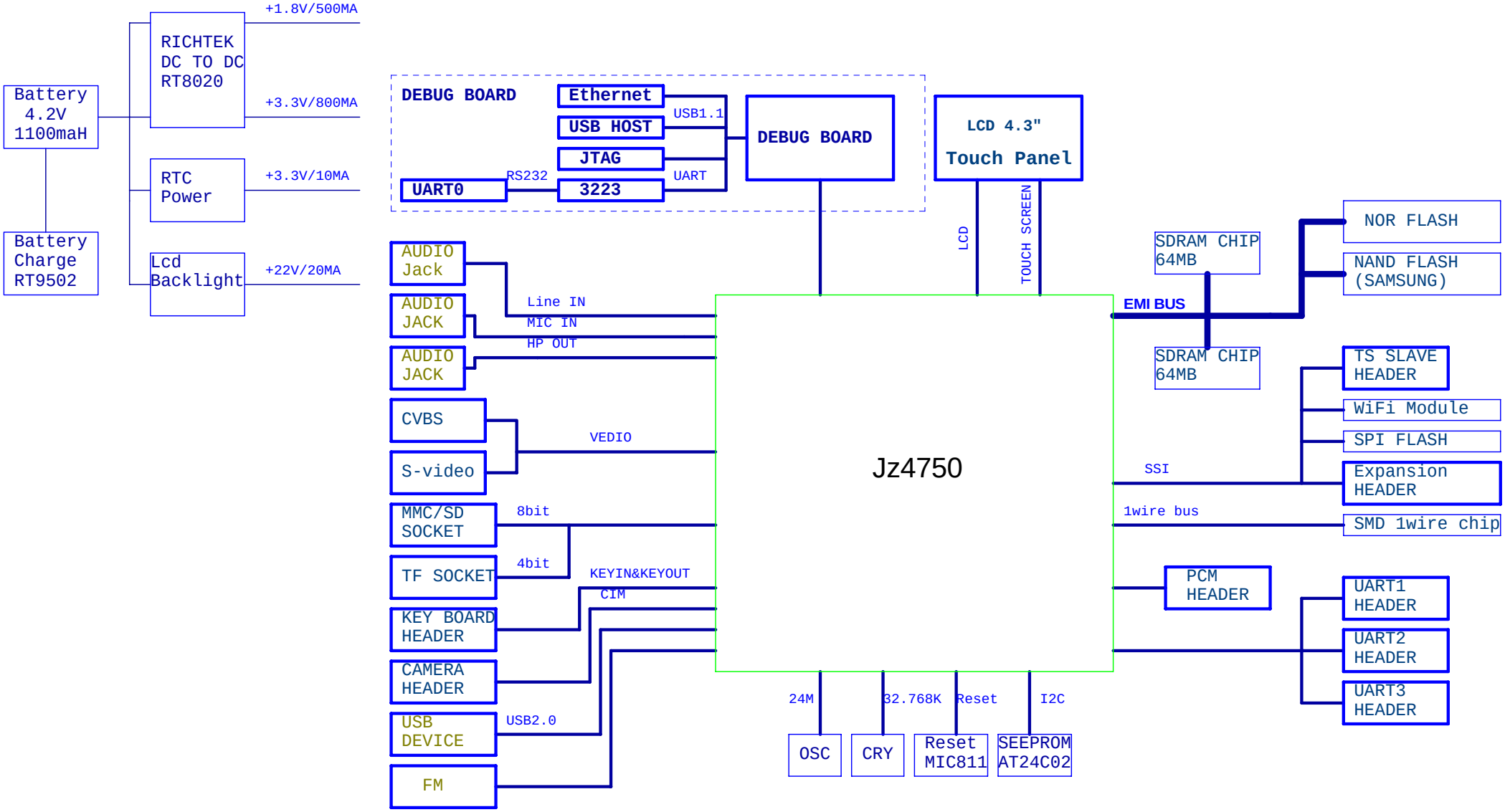
INGENIC Semiconductor Corporation

System Technology Department

Release Data:
2009.6.2

INGENIC Semiconductor Co.,Ltd.

Title: RD4750 APUS Board		
Size: B	Document Number: Cover	Rev: 1.2
Date: Wednesday, December 02, 2009	Sheet 1 of 12	



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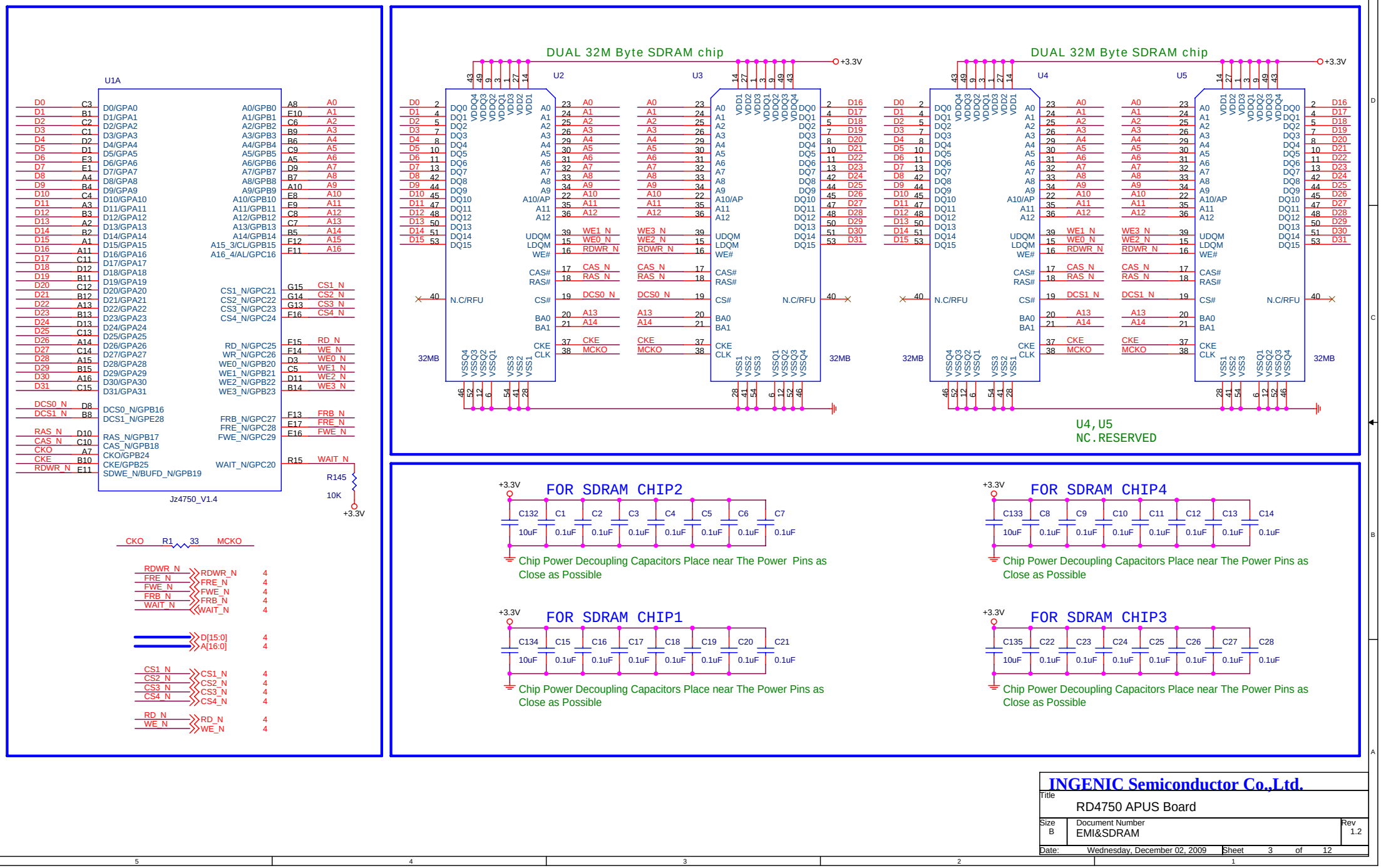
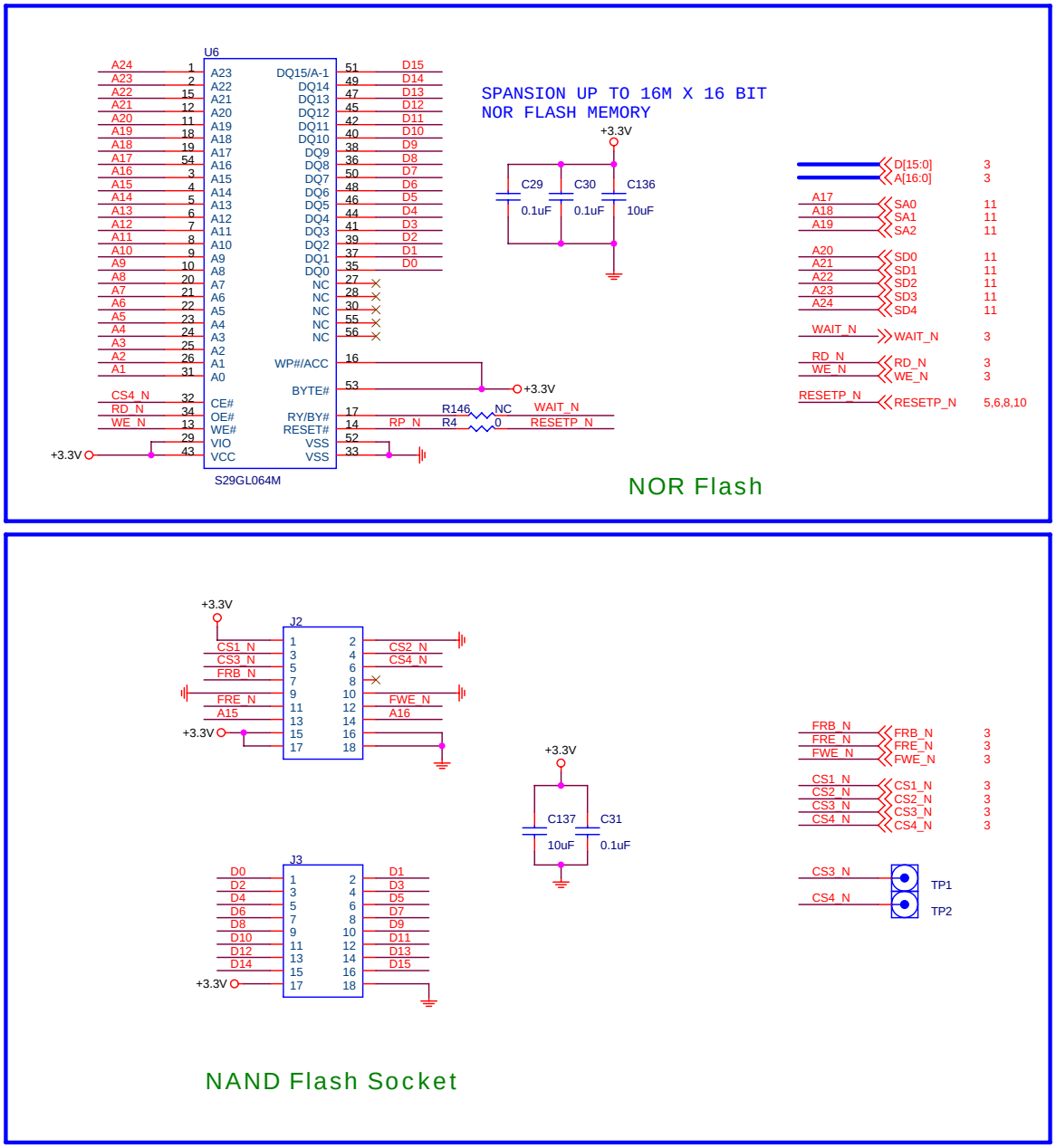
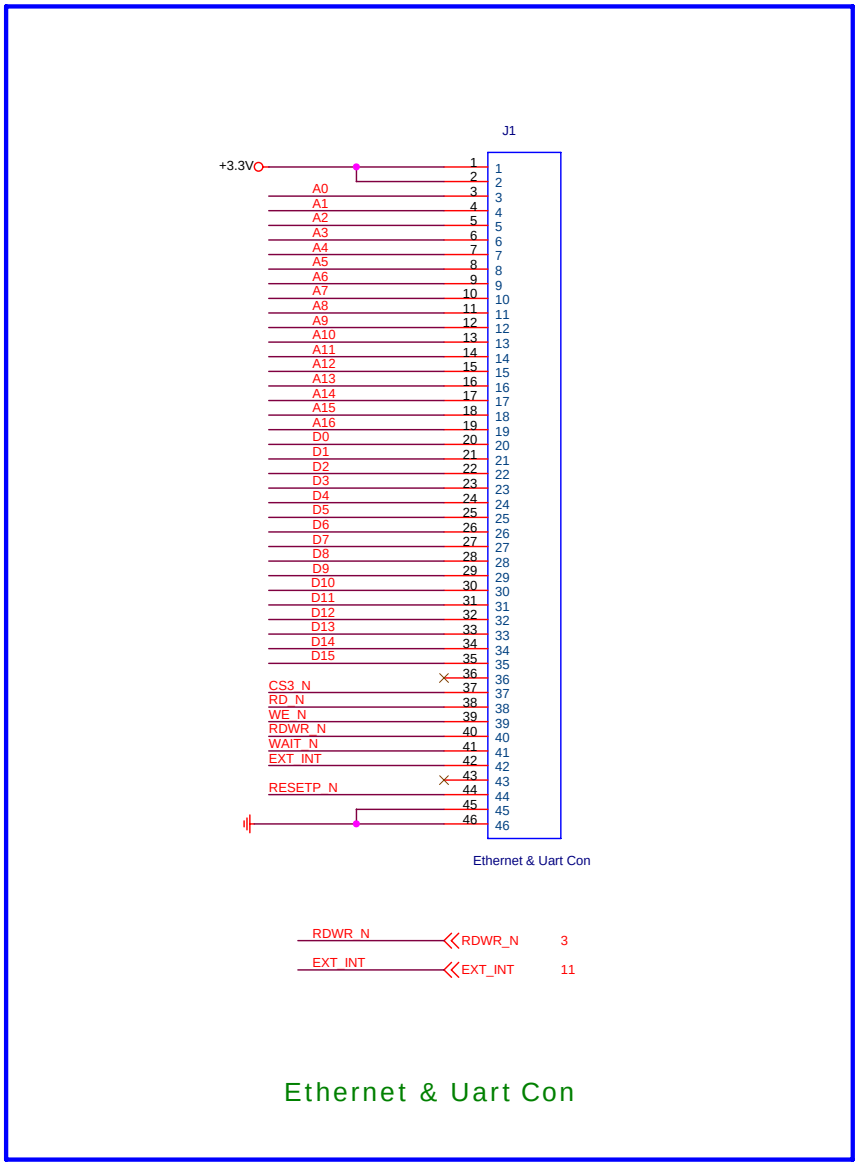
Title: RD4750 APUS Board		
Size: B	Document Number: System Architecture	Rev: 1.2
Date: Wednesday, December 02, 2009	Sheet 2 of 12	



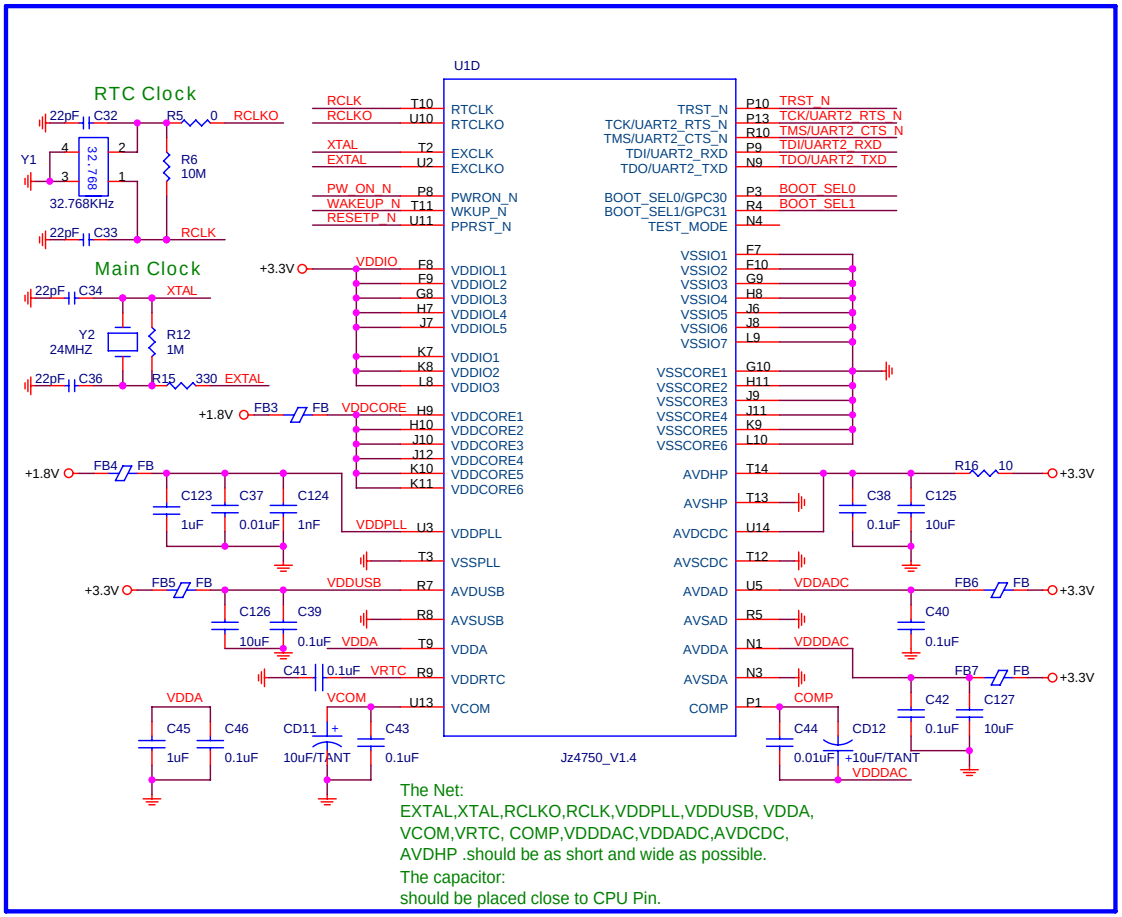
Title	RD4750 APUS Board
Size	B
Document Number	EMI&SDRAM
Rev	1.2
Date:	Wednesday, December 02, 2009
Sheet	3 of 12

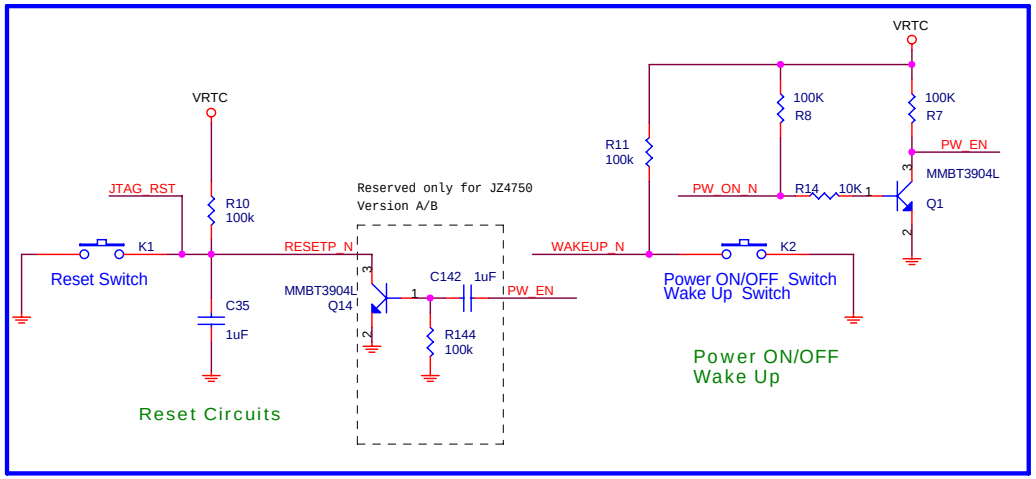
INGENIC Semiconductor Co.,Ltd.

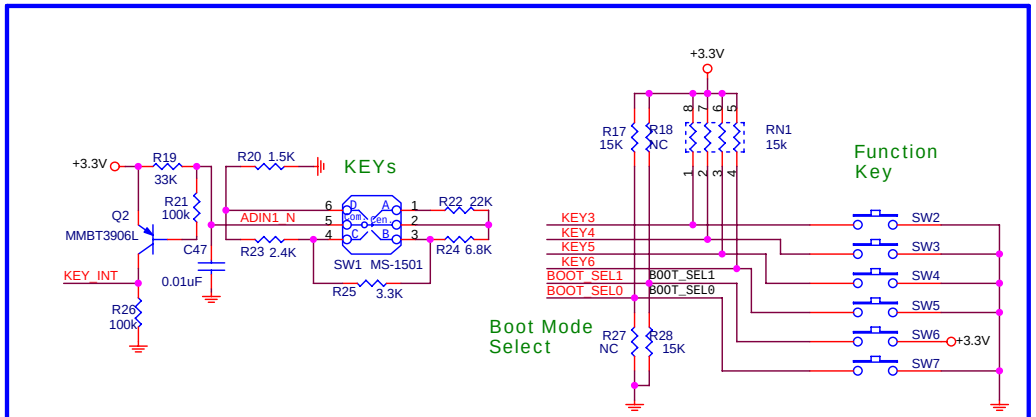




INGENIC Semiconductor Co.,Ltd.		
Title: RD4750 APUS Board		
Size: B	Document Number: Static Memory&Ethernet	Rev: 1.2
Date: Wednesday, December 02, 2009	Sheet 4 of 12	







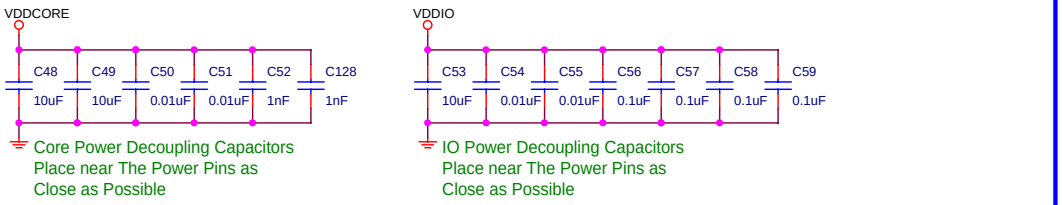
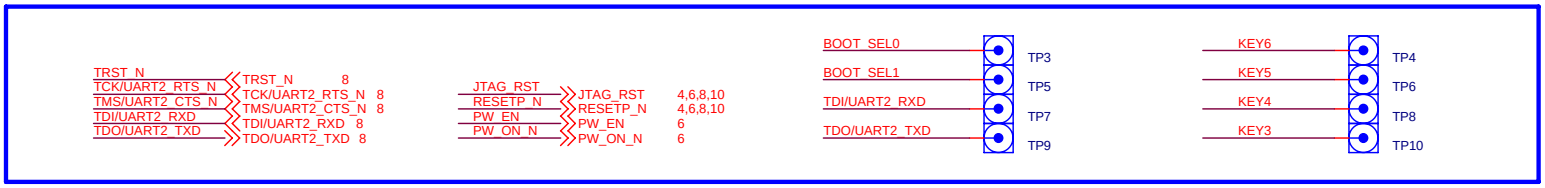


TABLE FOR BOOT MODE SELECT

BOOT_SEL[1:0]	
00	Boot from external ROM/Nor Flash at CS4 or from SPI Nor Flash at SSI0
01	Boot from NAND flash at CS1 (default setting)
10	Boot from SD Card at MMC0
11	Boot from USB device



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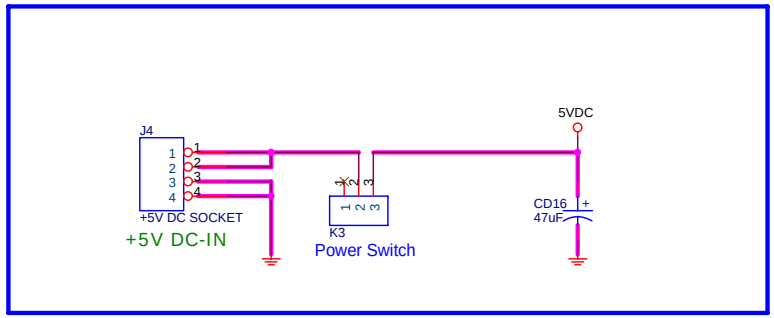
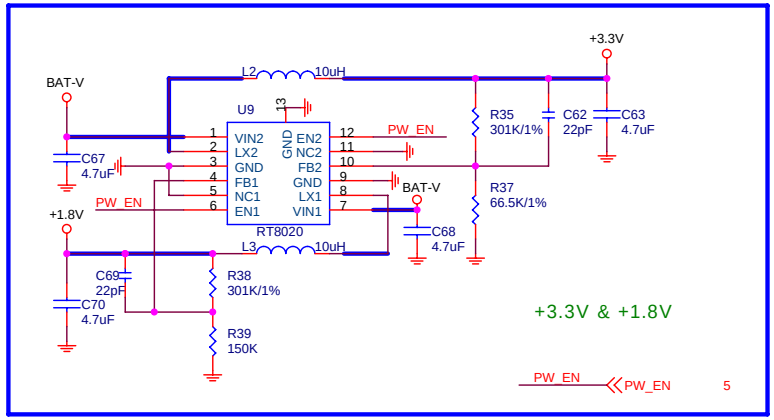
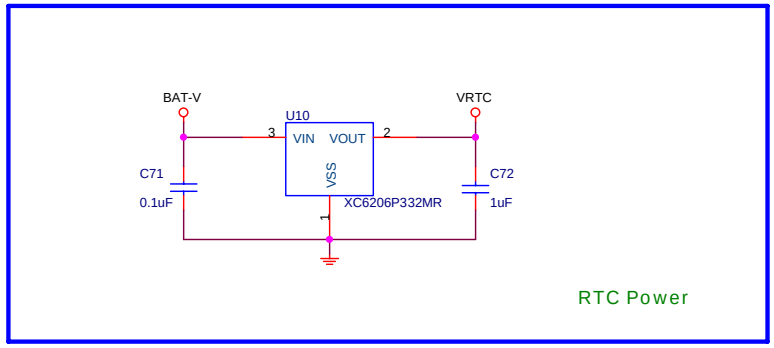
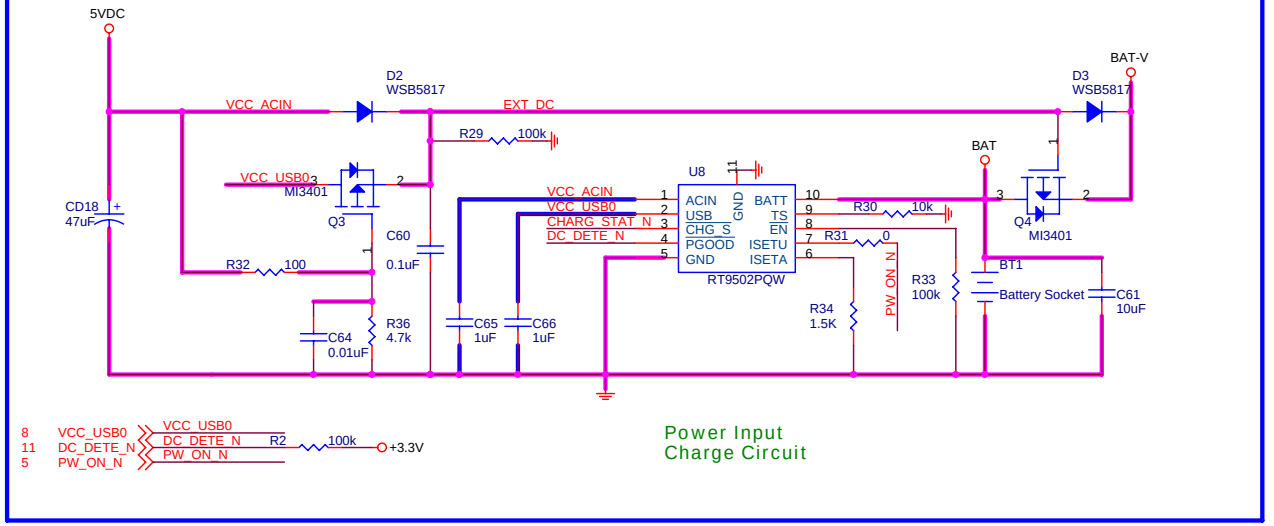
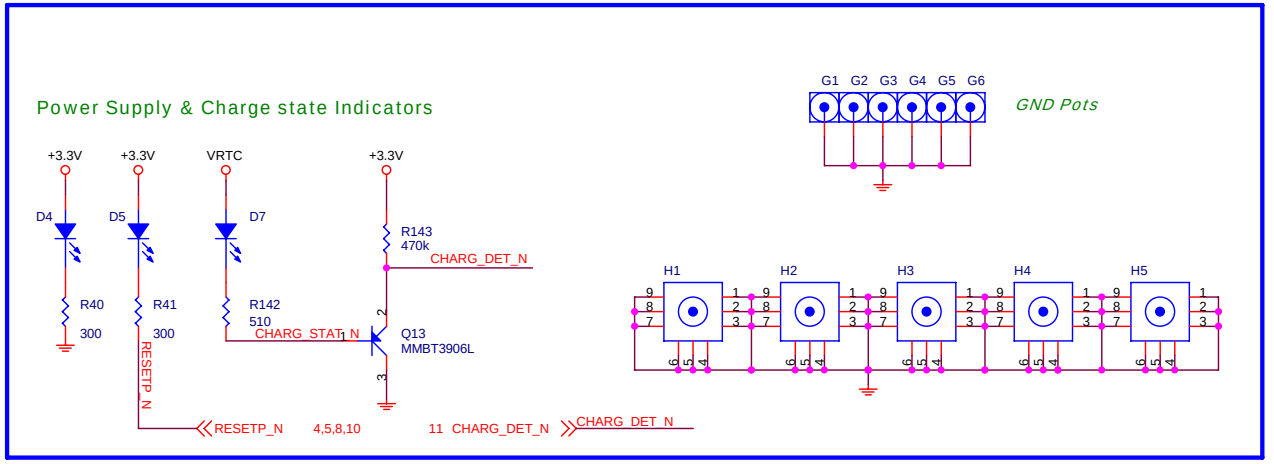
Title	RD4750 APUS Board	
Size B	Document Number Boot/Reset/Key	Rev 1.2
Date: Wednesday, December 02, 2009	Sheet 5 of 12	







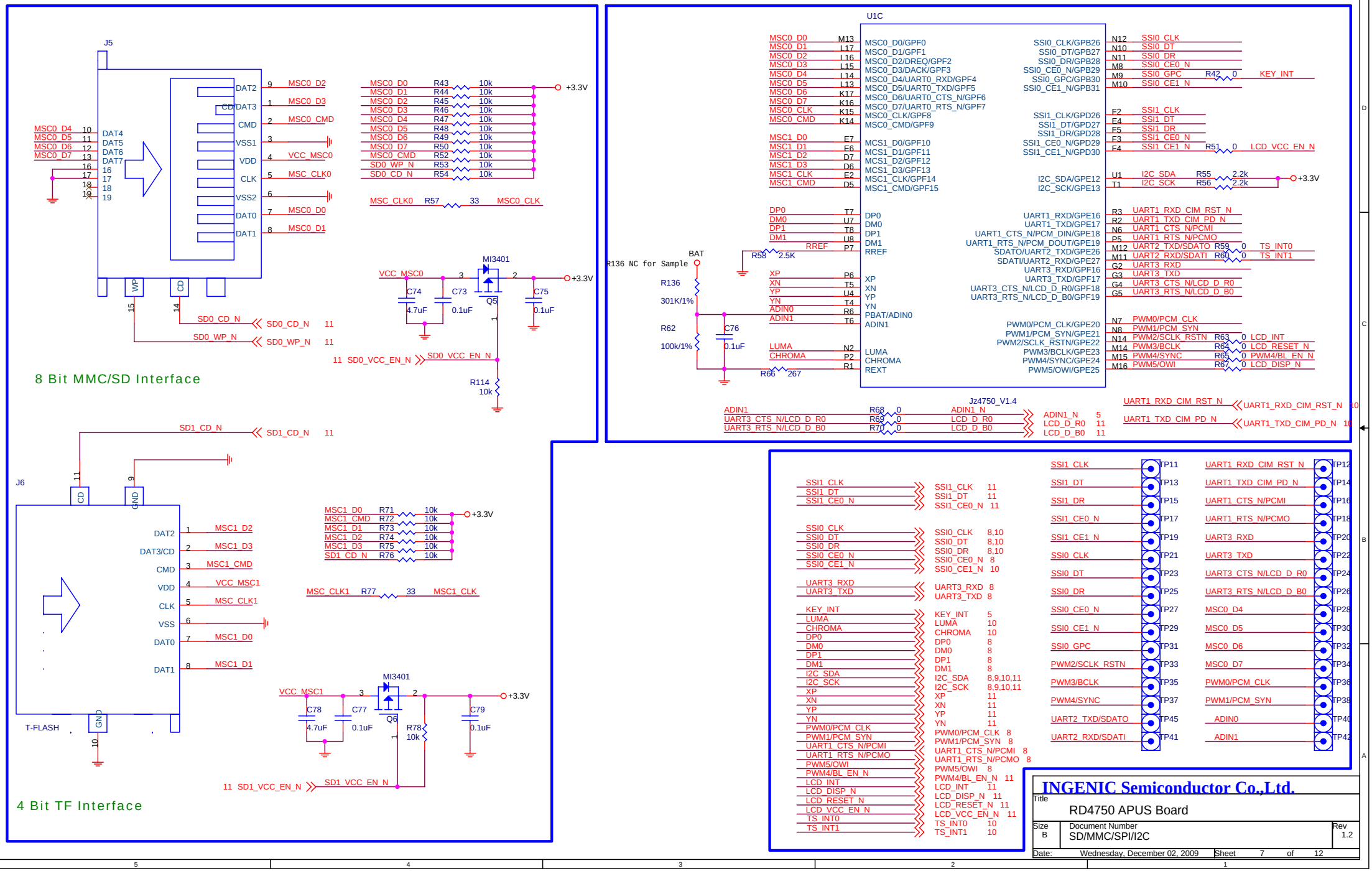




40mil 以上

60mil 以上

INGENIC Semiconductor Co.,Ltd.		
Title: RD4750 APUS Board		
Size: B	Document Number: POWER	Rev: 1.2
Date: Wednesday, December 02, 2009	Sheet 6 of 12	


8 Bit MMC/SD Interface
4 Bit TF Interface
U1C
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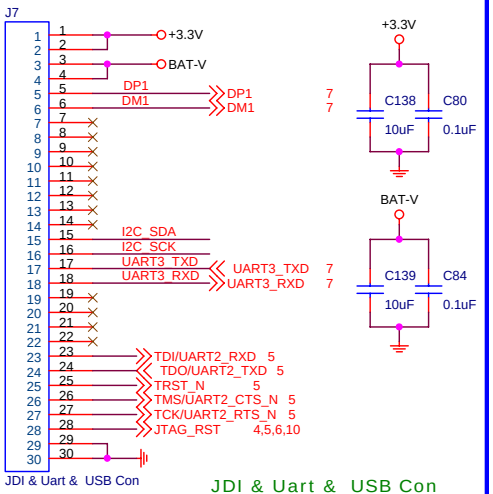
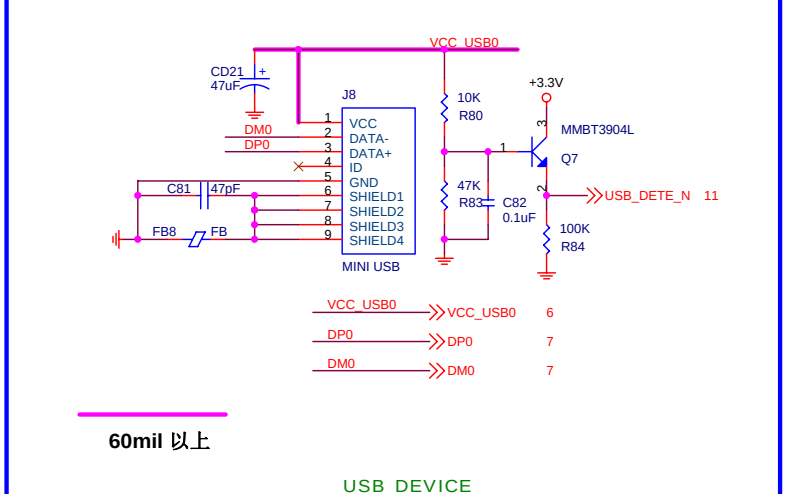
Title
RD4750 APUS Board
Size B
Document Number
SD/MMC/SPI/I2C
Rev 1.2
Date: Wednesday, December 02, 2009
Sheet 7 of 12

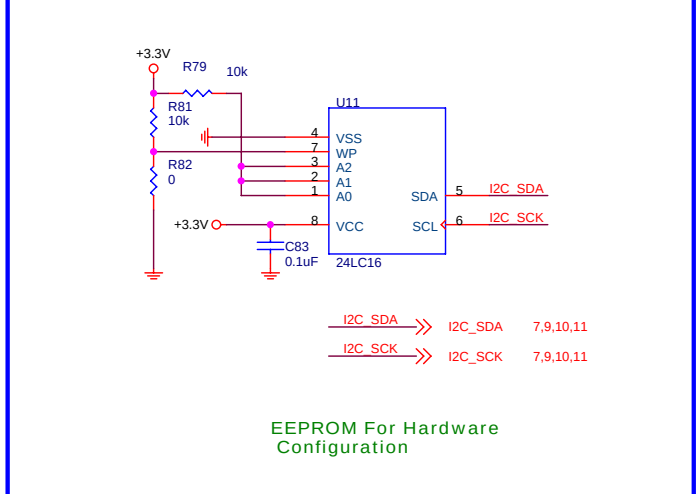


JDI & Uart & USB Con

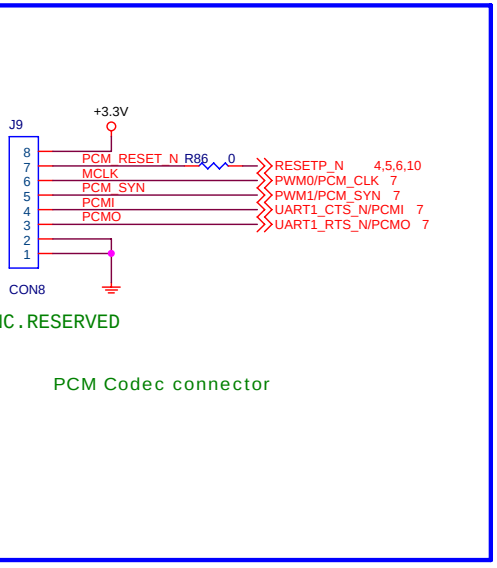


60mil 以上

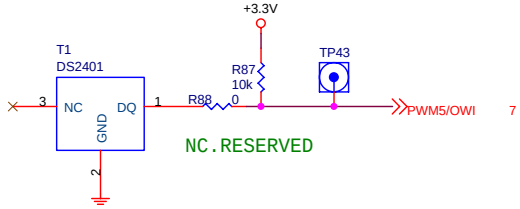
USB_DEVICE



EEPROM For Hardware Configuration

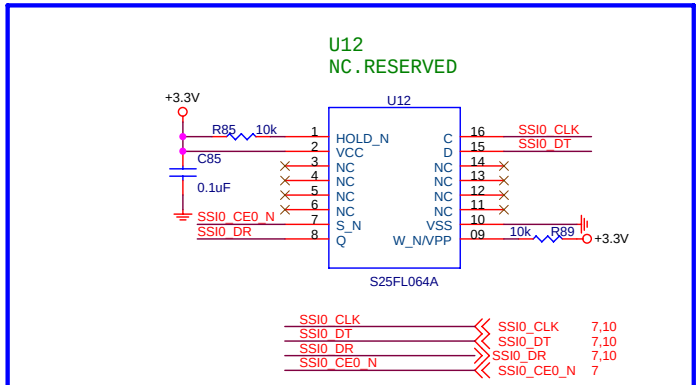


PCM Codec connector



One Wire Bus

U12
NC.RESERVED



SPI NorFlash for Boot

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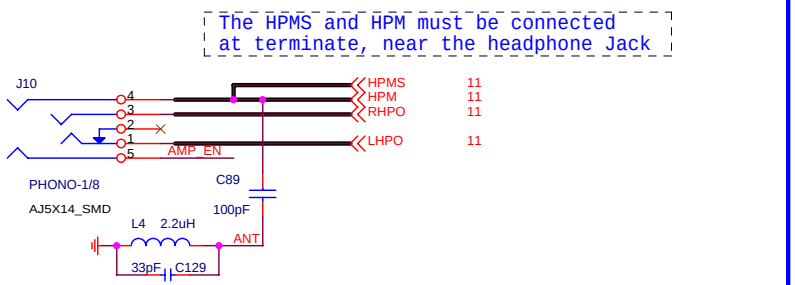
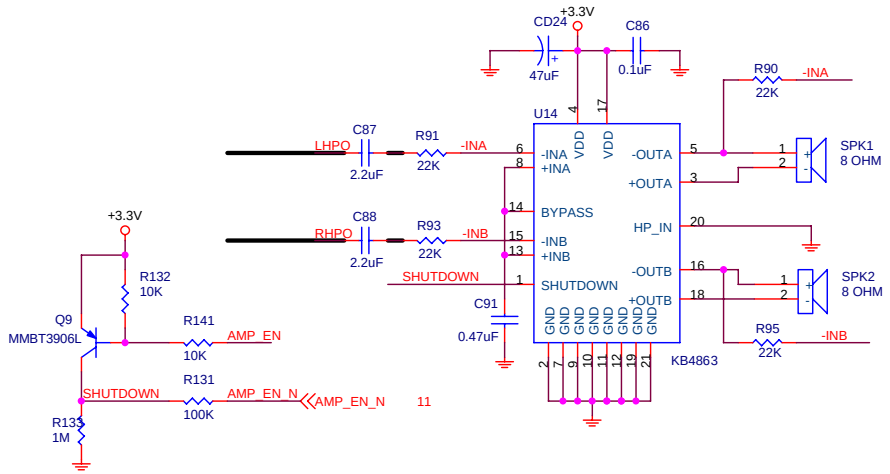
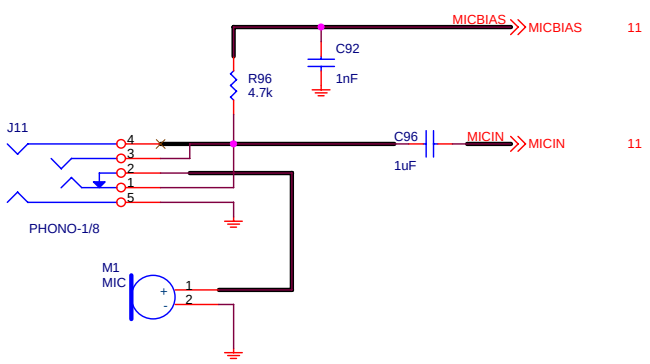
Title: RD4750 APUS Board		
Size: B	Document Number: JTAG/UART/USB/One Wire	Rev: 1.2
Date: Wednesday, December 02, 2009	Sheet 8 of 12	



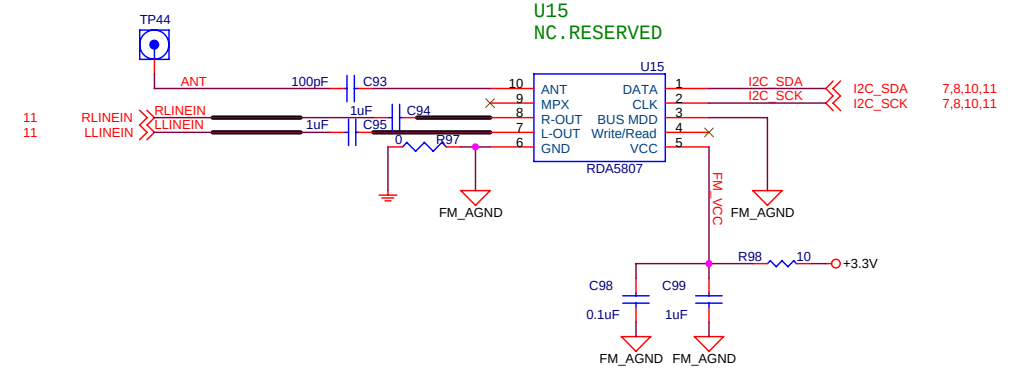
HEADPHONE



Speaker Out
Connector



MIC IN



FM MODULE

Surround with GND

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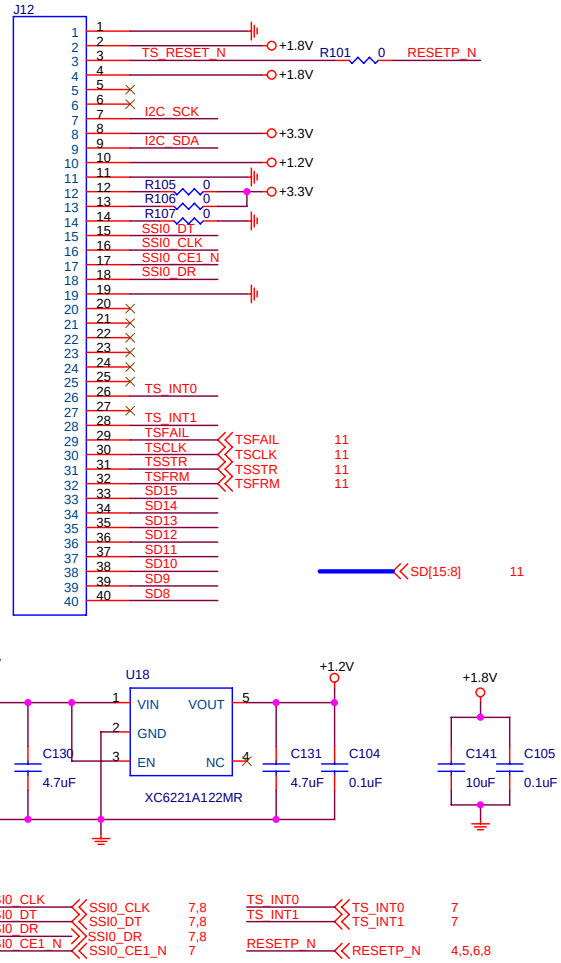
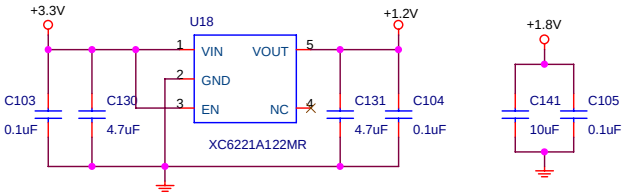
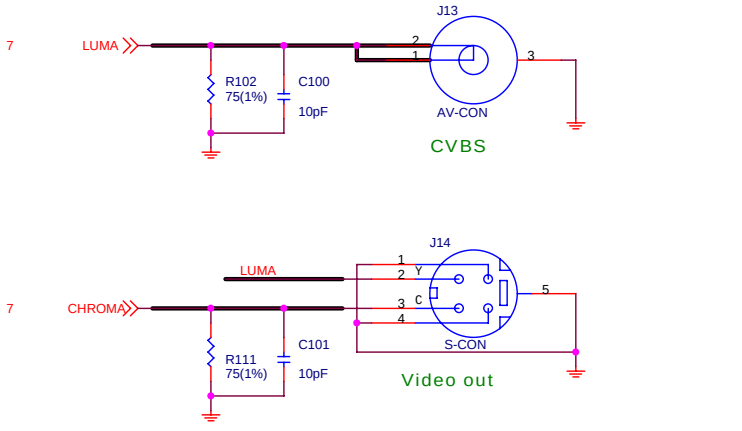
Title: RD4750 APUS Board		
Size: B	Document Number: Audio/FM	Rev: 1.2
Date: Wednesday, December 02, 2009	Sheet 9 of 12	

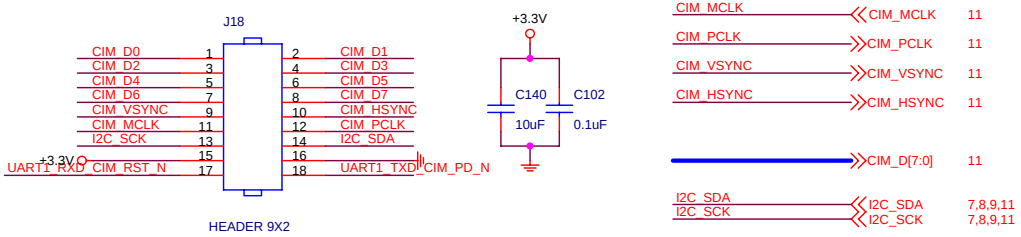
TS Interface Connector

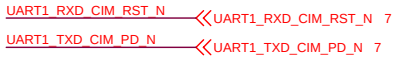






Camera interface Connector





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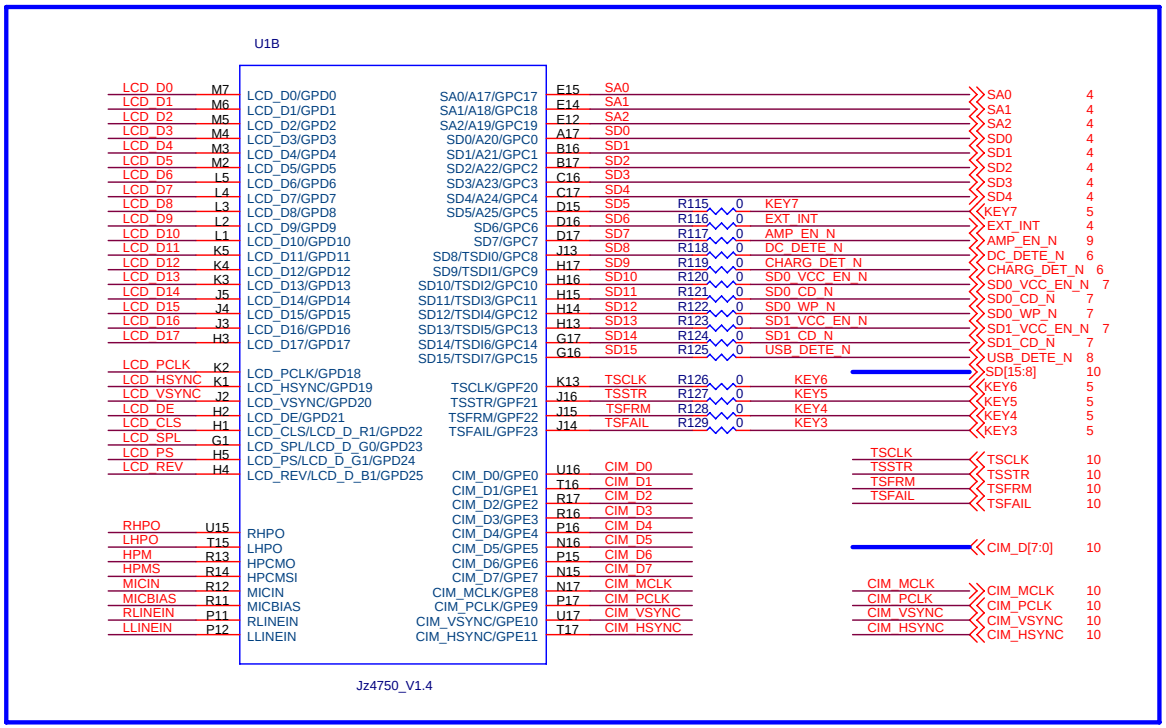
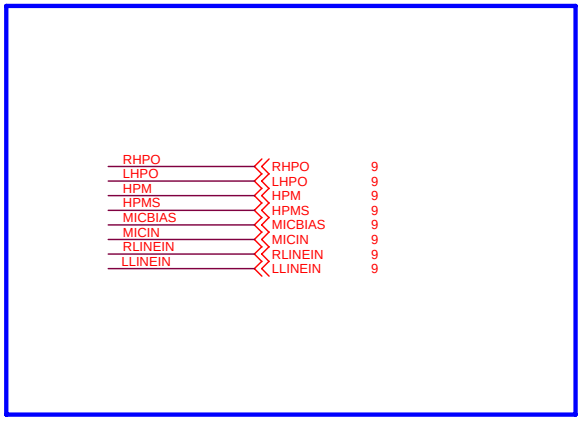
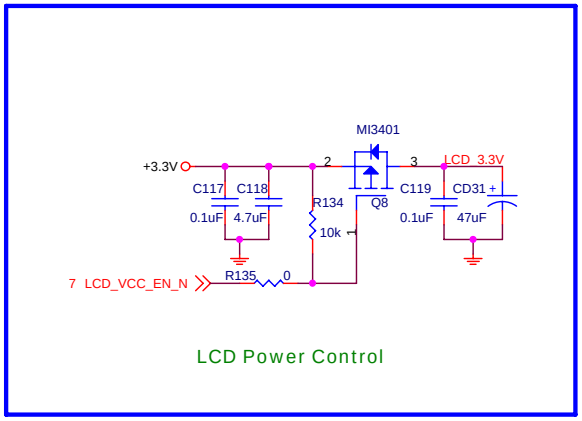
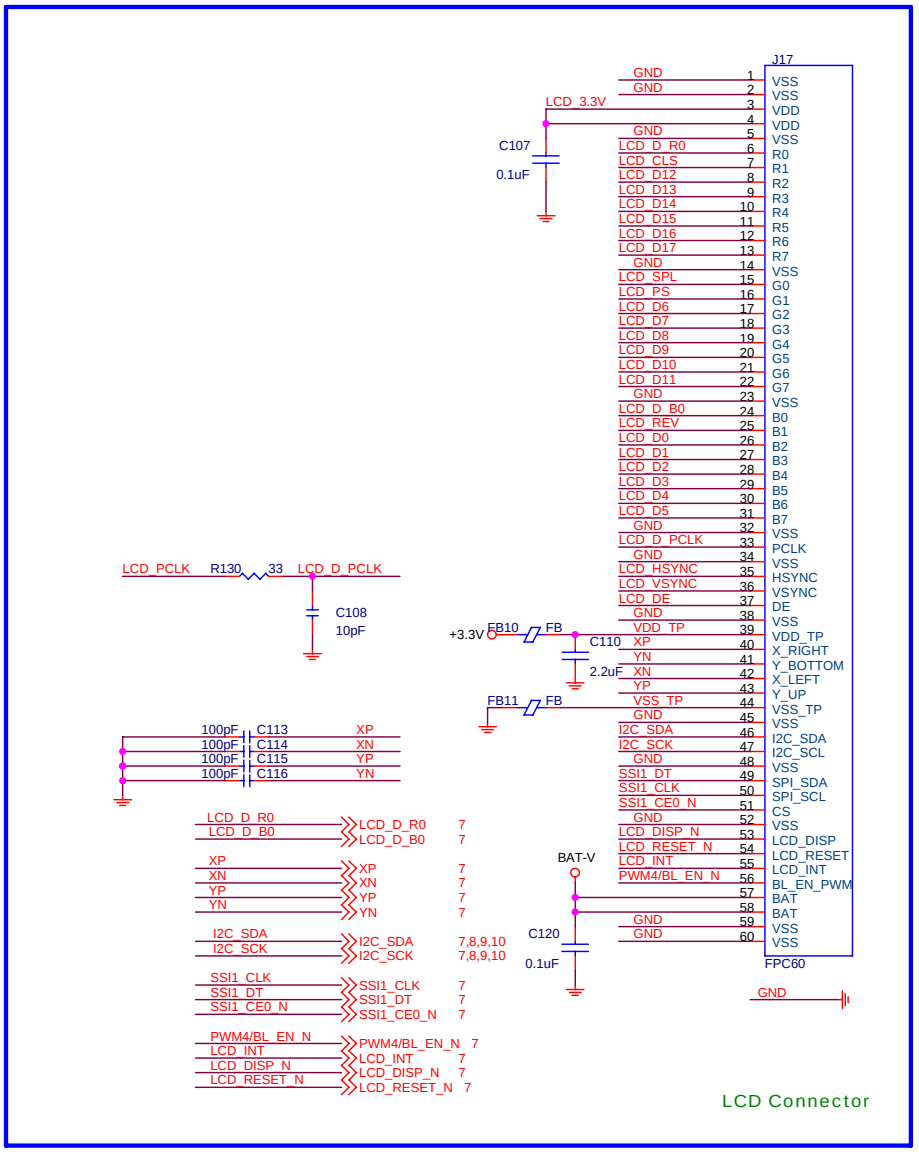
Title: RD4750 APUS Board		
Size: B	Document Number: TS/Video/Camera	Rev: 1.2
Date: Wednesday, December 02, 2009	Sheet 10 of 12	









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Title: RD4750 APUS Board		
Size: B	Document Number: LCD	Rev: 1.2
Date: Wednesday, December 02, 2009	Sheet 11 of 12	

Data	Revision	Change
Aug. 21 2008	Rev1.0	1. First released Revision
Oct. 13 2008	Rev1.1	1. PAGE07: Add a switch circuit: "cut off BAT ADCIN" to avoid leak current 2. PAGE05 :Change the Jz4750 decouple capcitors 3. PAGE06: Add a battery charge-state LED"D7" indicator circuit 4. PAGE06: Remove Main 5V polyswitch-Type Fuse near K3 5. PAGE09: Add a base current limit resistor for "AMP_EN" BJT transistor Q9 6. PAGE05: Define the default Boot from NAND, boot_SEL [1:0]01. 7. PAGE05: change the Key6 push to active high. 8. PAGE08: Redefine "T1"DS2401 pinout 9. PAGE10: Add two control pins for Camera interface Connector "J18", using GPIO UART1
Dec. 10 2008	Rev1.1.1	1. PAGE07: Change the BAT detect circuit 2. PAGE09: Change R96 from 2.2k to 4.7k 3. PAGE09: Change ANT circuit 4. PAGE05: Change R15 from 0 to 330 Ohm 5. PAGE05: Add Q14,C142,R144. Reserved only for JZ4757 Version A/B 6. PAGE03: Add R145 pullup resistor 7. PAGE04: Add R146 for software busy query
Feb. 11 2009	Rev1.1.2	1. PAGE09: Exchange the places of LHPO and RHPO
Apr. 15 2009	Rev1.1.3	1. PAGE11: Exchange LHPO's and RHPO's PIN numbers in Jz4750 source package. 2. PAGE11: Exchange AIL's and AIR's PIN numbers in Jz4750 source package.
Jun. 1 2009	Rev1.2	1. Just change the revision number for PCB's change.

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Title: RD4750 APUS Board

Size: B | Document Number: History | Rev: 1.2

Date: Wednesday, December 02, 2009 | Sheet 12 of 12