

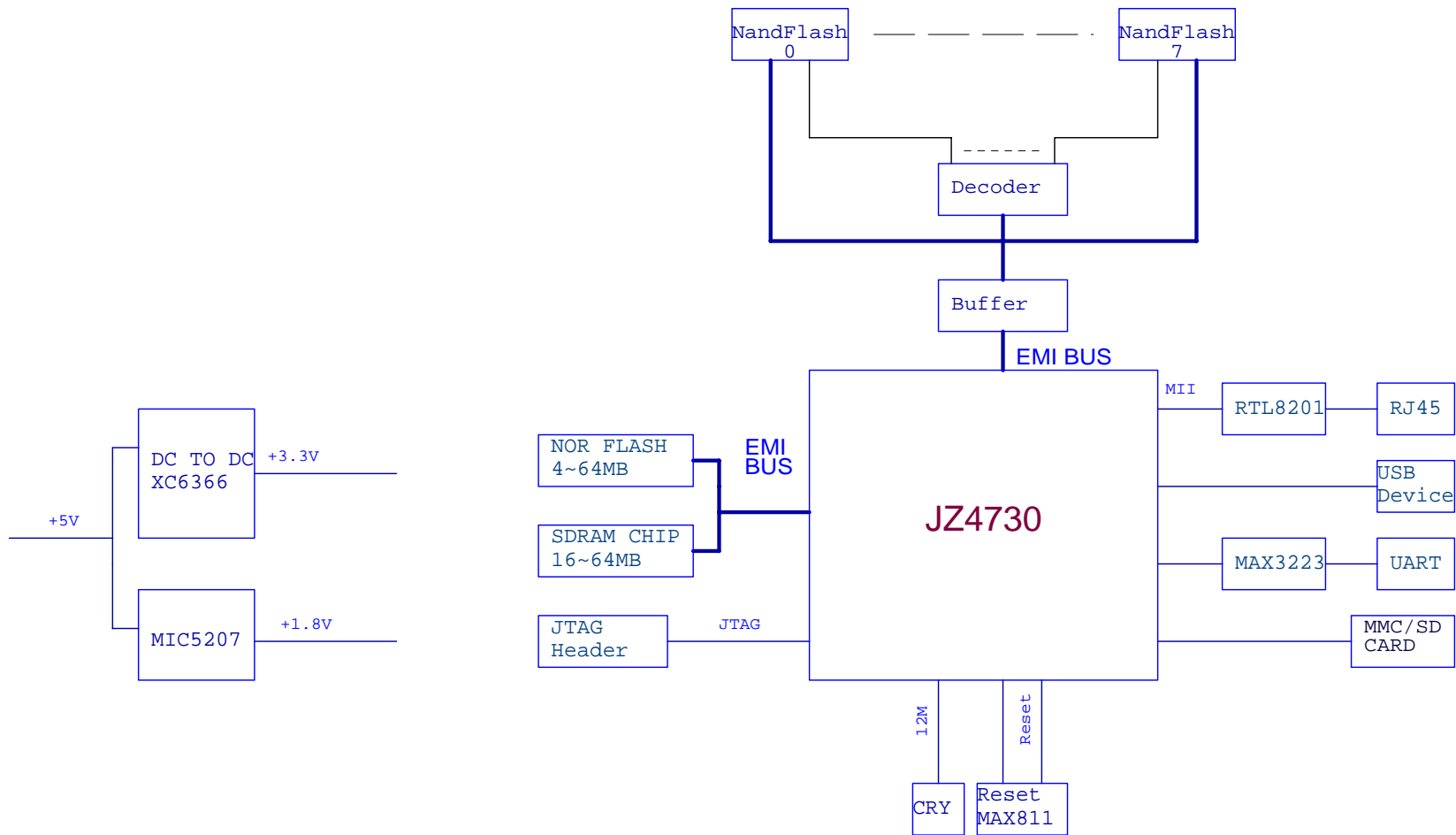


NAND Flash Programmer with Jz4730 Schematic Revision 1.0

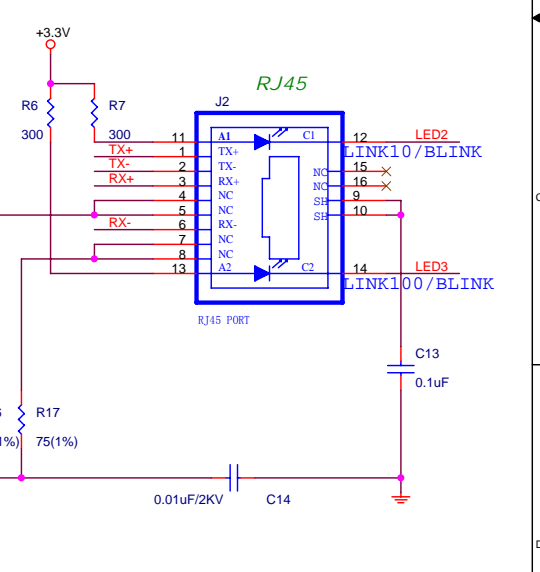
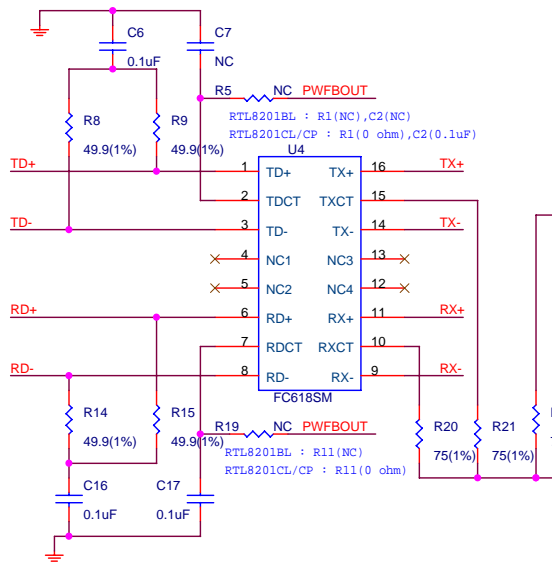
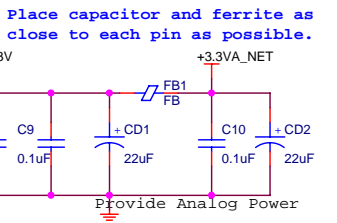
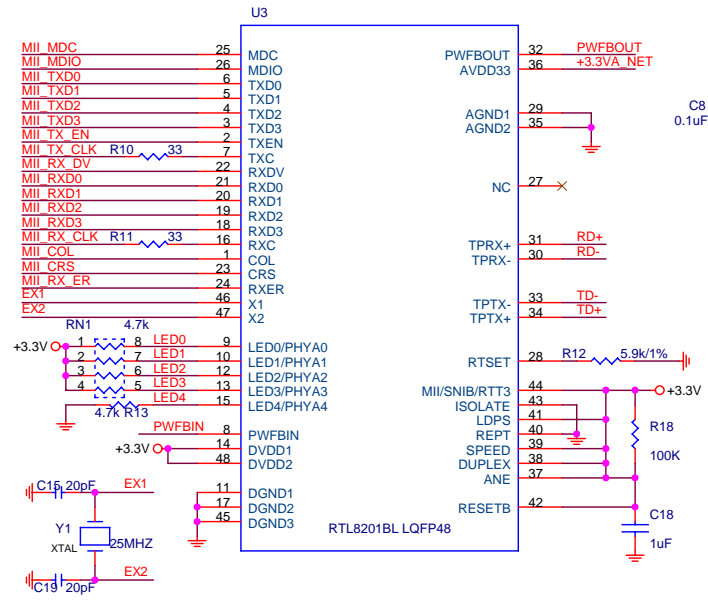
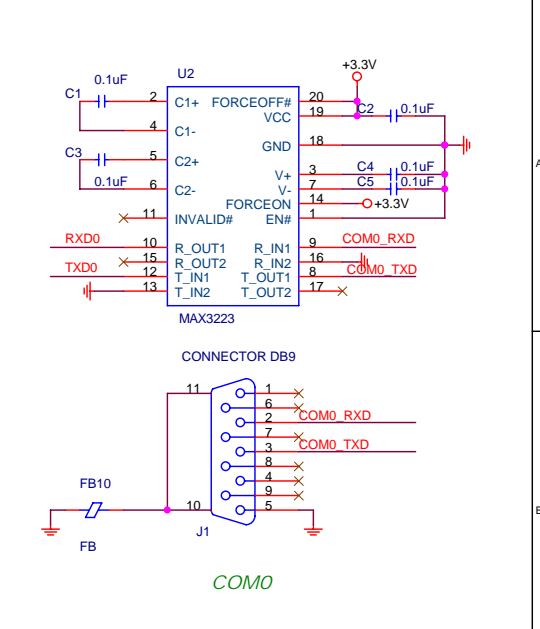
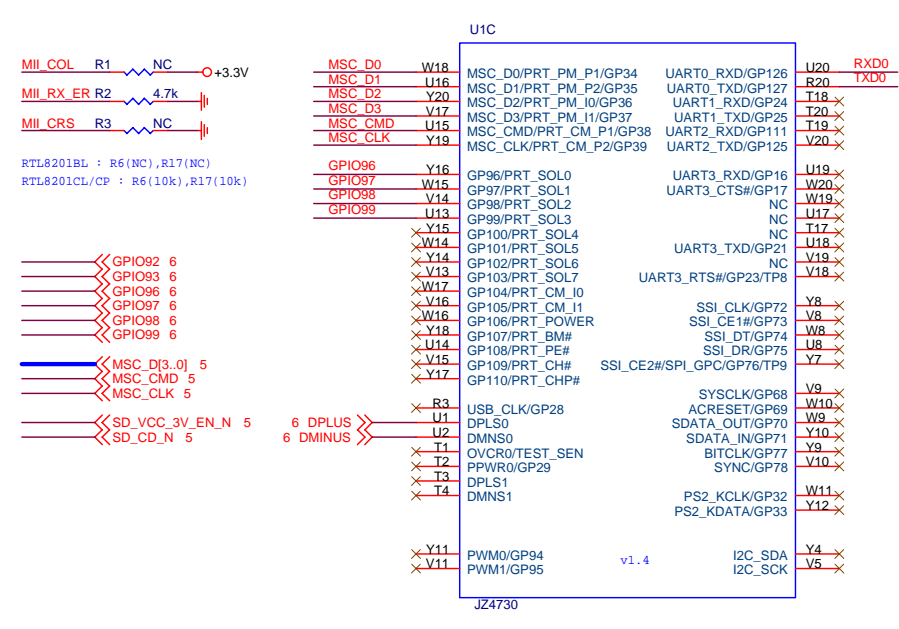
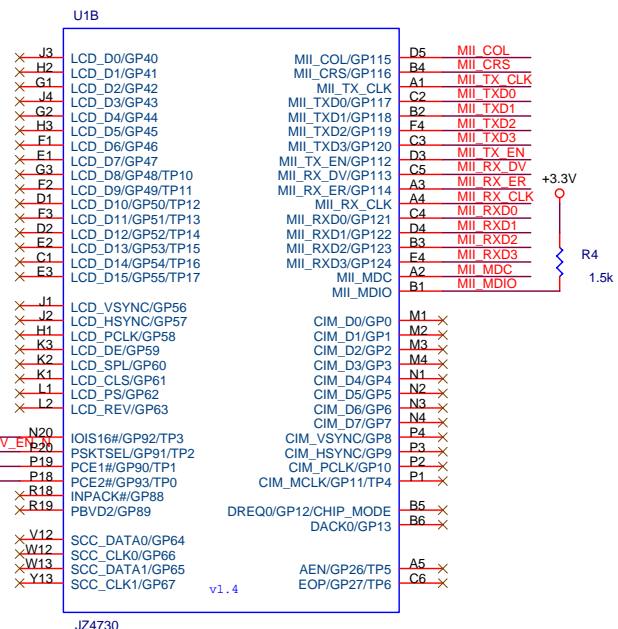
| Title | Page |
|---------------------|------|
| Cover Sheet | 1 |
| System Architecture | 2 |
| Ethernet & UART | 3 |
| Memory & Buffer | 4 |
| NAND Flash & SD | 5 |
| Power & JTAG | 6 |
| Revision History | 7 |

INGENIC Semiconductor Corporation
System Technology Department
Release Data:
2007.6.12

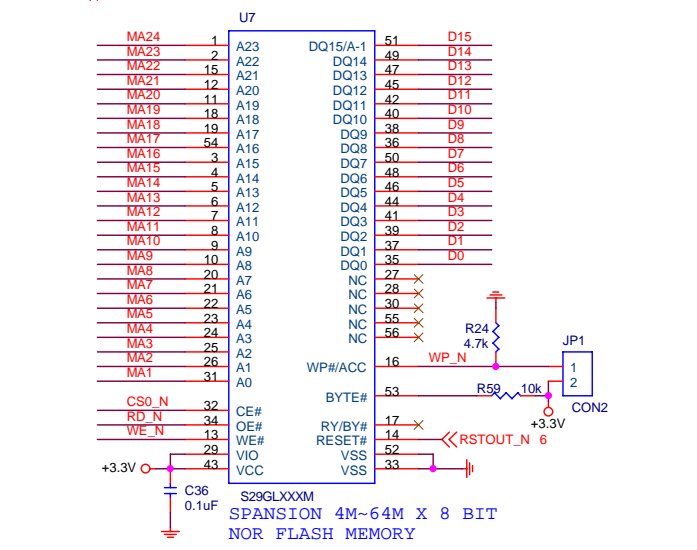
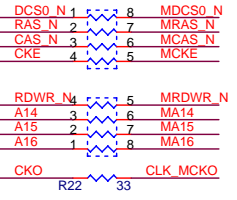
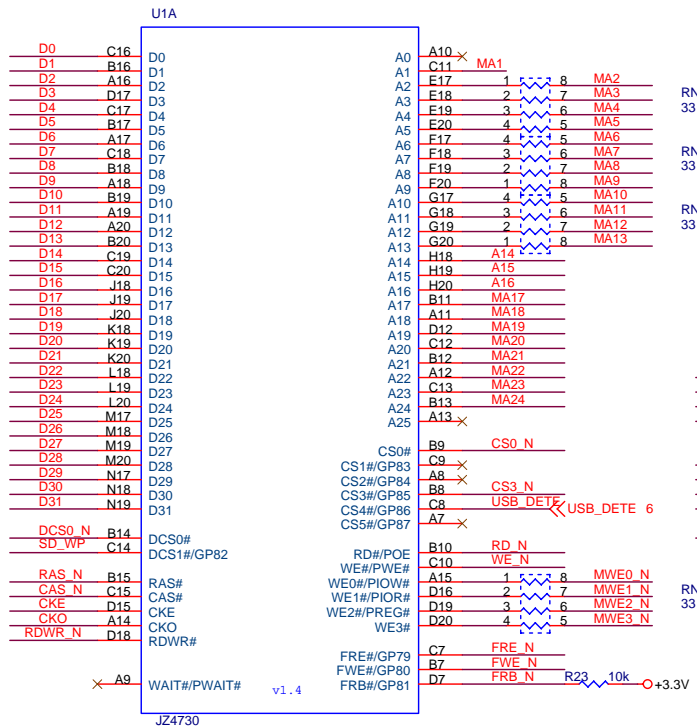
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| INGENIC Semiconductor Co. Ltd | | |
| Title | PD4730_NAND Flash Programmer | |
| Size | Document Number | Rev |
| B | COVER | 1.0 |
| Date: | Tuesday, July 24, 2007 | Sheet 1 of 7 |



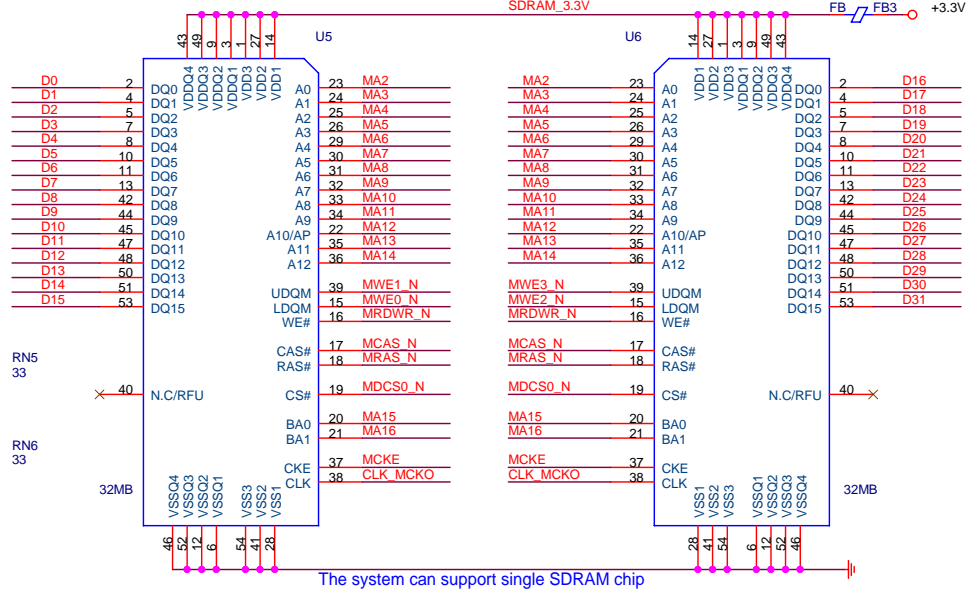
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| INGENIC Semiconductor Co. Ltd | | |
| Title | | |
| PD4730_NAND Flash Programmer | | |
| Size | Document Number | Rev |
| B | System Architecture | 1.0 |
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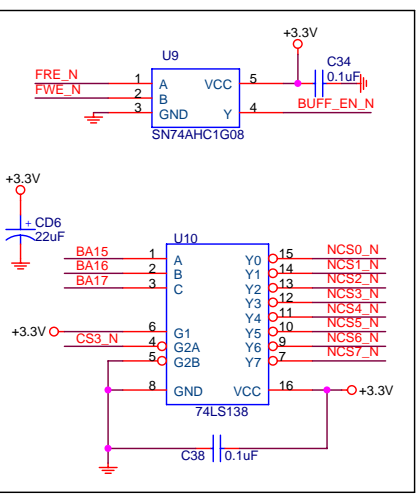
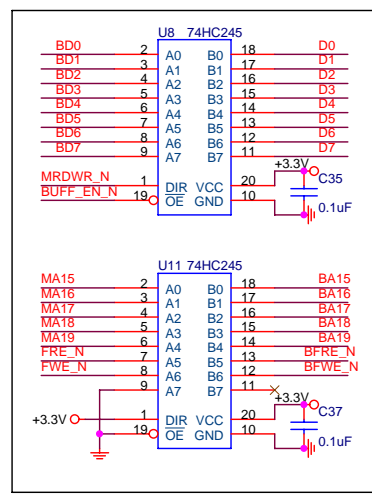
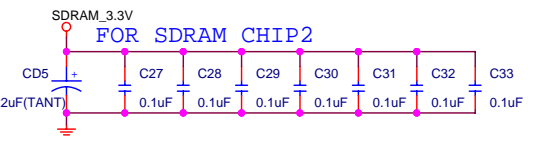
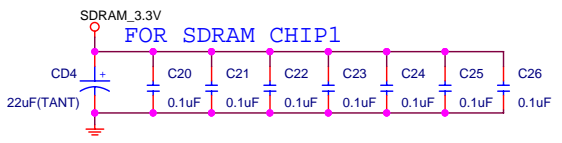
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| INGENIC Semiconductor Co. Ltd | | |
| Title | | |
| PD4730_NAND Flash Programmer | | |
| Size | Document Number | Rev |
| B | Ethernet & UART | 1.0 |
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DUAL 32M Byte SDRAM chip

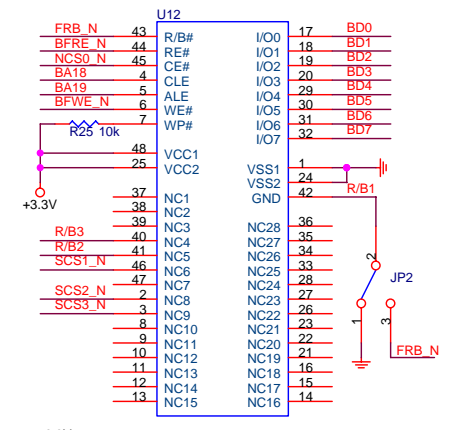


The system can support single SDRAM chip

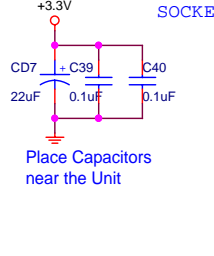


- NCS0_N 5
- NCS1_N 5
- NCS2_N 5
- NCS3_N 5
- NCS4_N 5
- NCS5_N 5
- NCS6_N 5
- NCS7_N 5
- BA18 5
- BA19 5
- BFRE_N 5
- BFWE_N 5
- FRB_N 5
- BD[7..0] 5

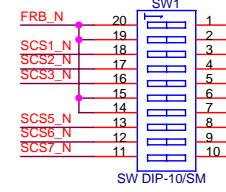
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|-------------------------------|------------------------------|
| Title | PD4730 NAND Flash Programmer |
| Size | Document Number |
| B | Memory & Buffer |
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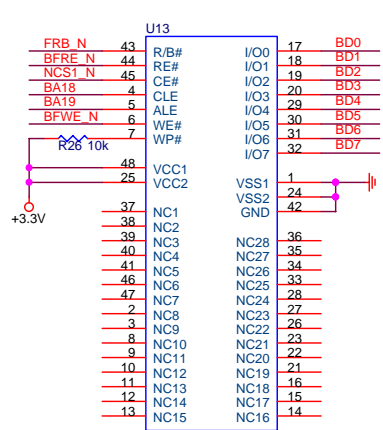
SOCKET BOARD 1



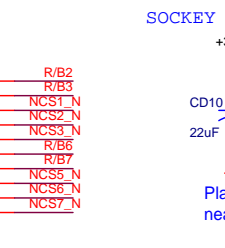
Place Capacitors near the Unit



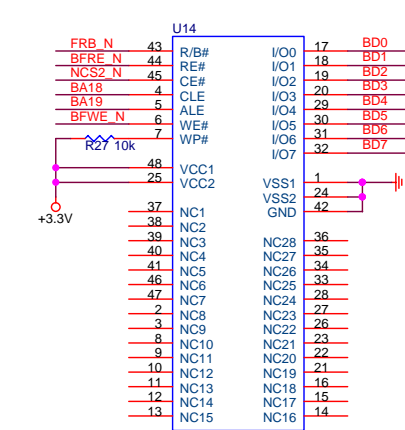
SW DIP-10/SM



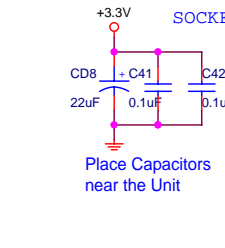
SOCKET BOARD 2



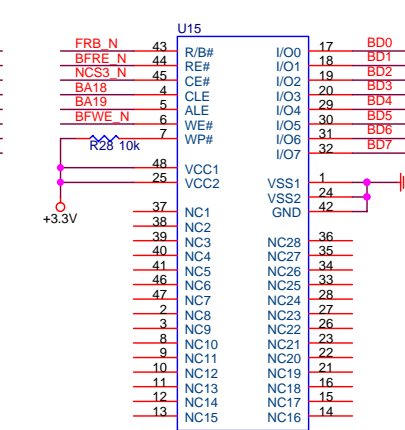
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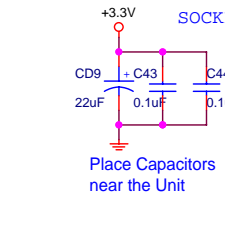
SOCKET BOARD 3



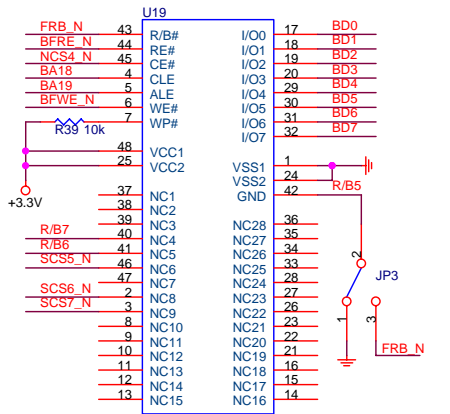
Place Capacitors near the Unit



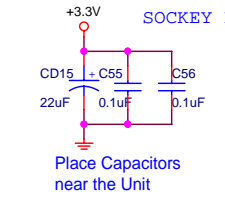
SOCKET BOARD 4



Place Capacitors near the Unit



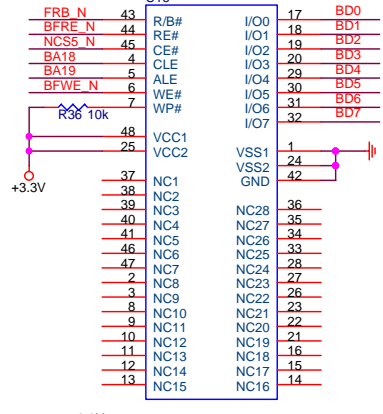
SOCKET BOARD 5



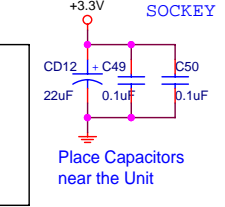
Place Capacitors near the Unit

1-CS chip:
JP2, JP3= 1-2
SW1= OFF

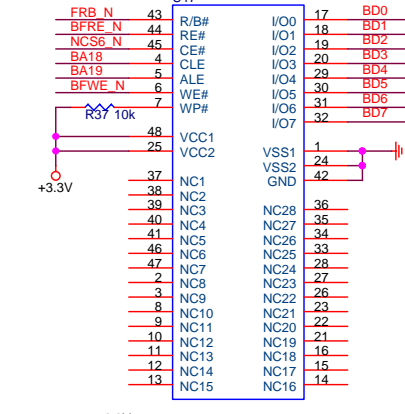
4-CS chip:
JP2, JP3= 2-3
SW1= ON
socket 2-4, 6-8 do not use



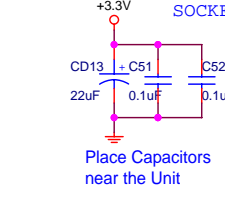
SOCKET BOARD 6



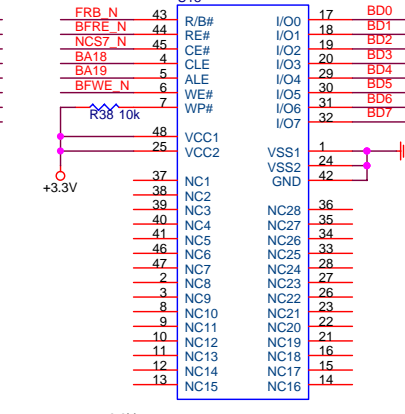
Place Capacitors near the Unit



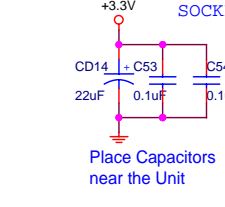
SOCKET BOARD 7



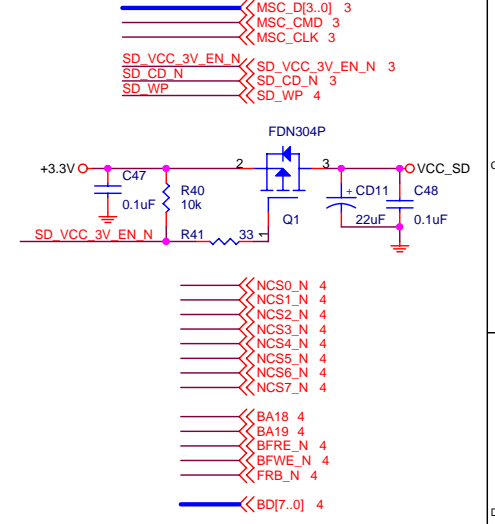
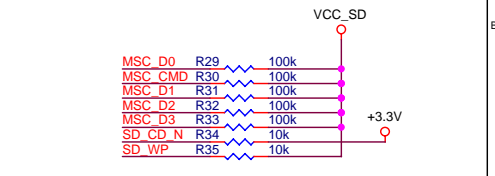
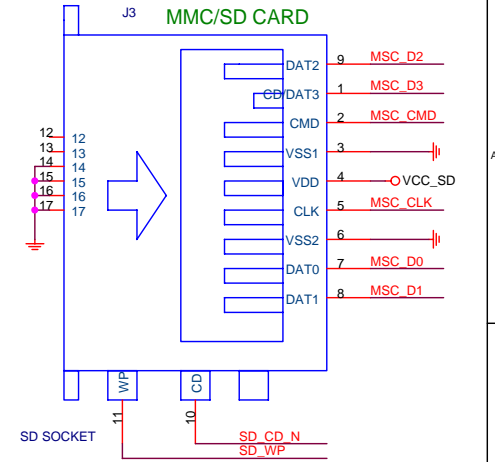
Place Capacitors near the Unit



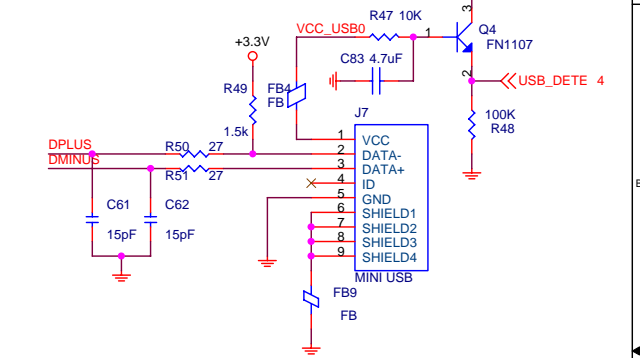
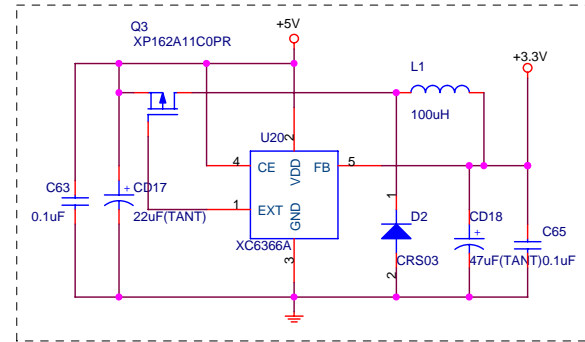
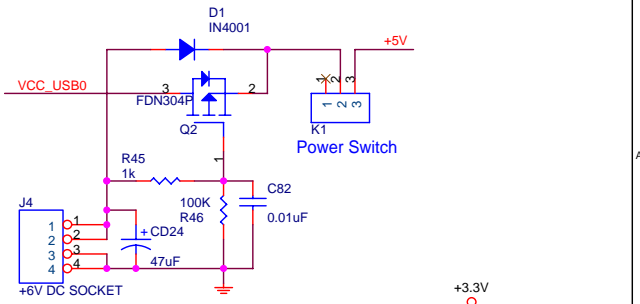
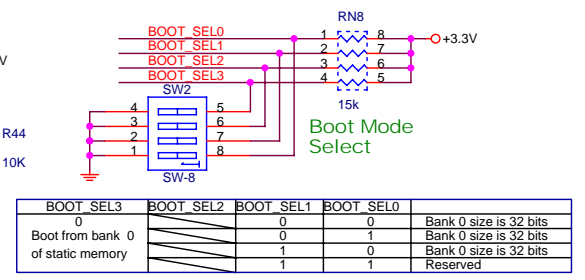
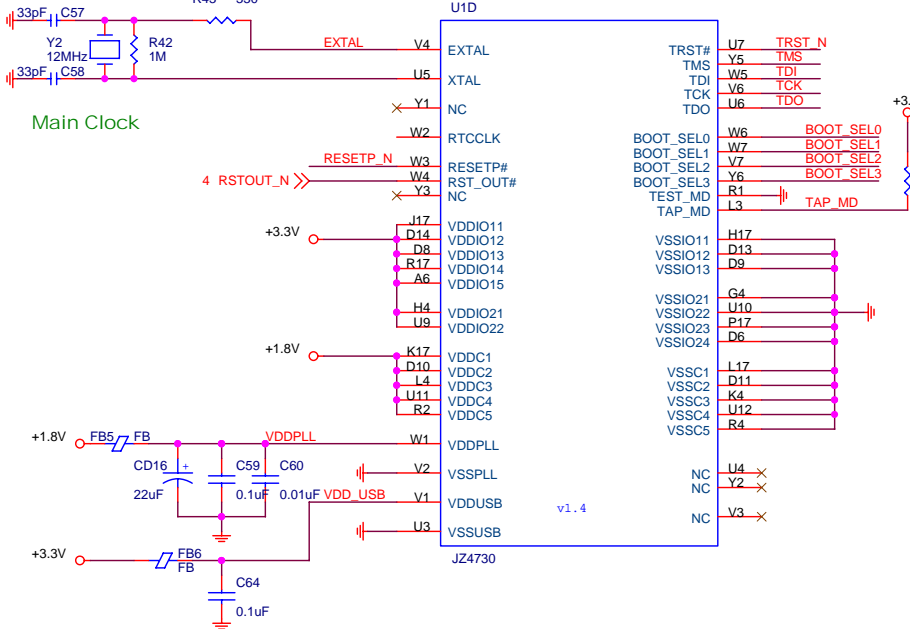
SOCKET BOARD 8



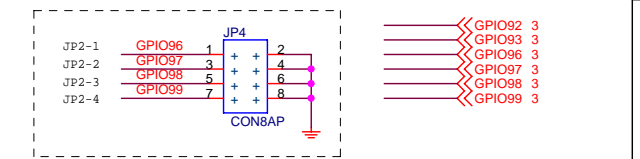
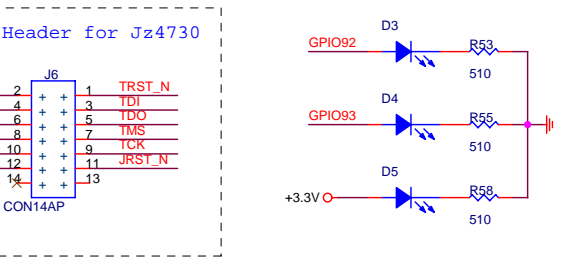
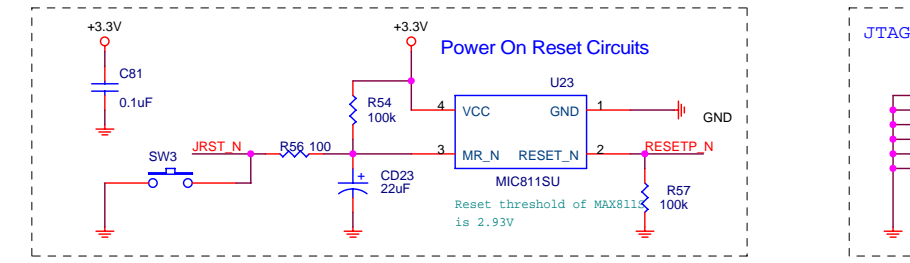
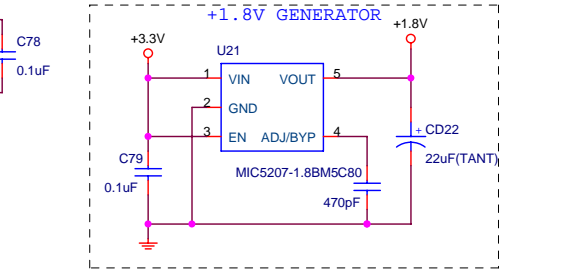
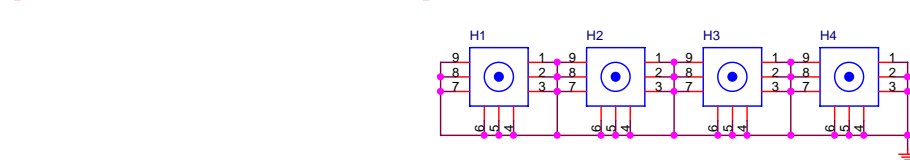
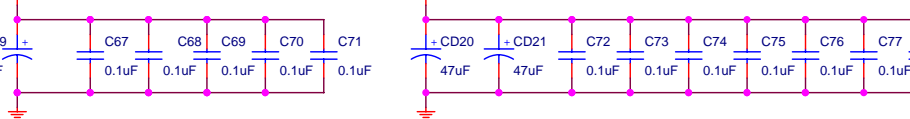
Place Capacitors near the Unit



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| INGENIC Semiconductor Co. Ltd | | |
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| Date: | Tuesday, July 24, 2007 | Sheet 5 of 7 |



Core Power Decoupling Capacitors
Place near The Power Pins as Close as Possible



INGENIC Semiconductor Co. Ltd

Title: PD4730_NAND Flash Programmer

Size B: Document Number: Power & JTAG Rev 1.0

Date: Tuesday, July 24, 2007 Sheet 6 of 7

Data

Revision

Change

| Jun. 08 2007 | Rev1.0 | 1. First released Revision |
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INGENIC Semiconductor Co. Ltd

| | | |
|------------------------------|------------------------|--------------|
| Title | | |
| PD4730_NAND Flash Programmer | | |
| Size | Document Number | Rev |
| B | Revision History | 1.0 |
| Date: | Tuesday, July 24, 2007 | Sheet 7 of 7 |