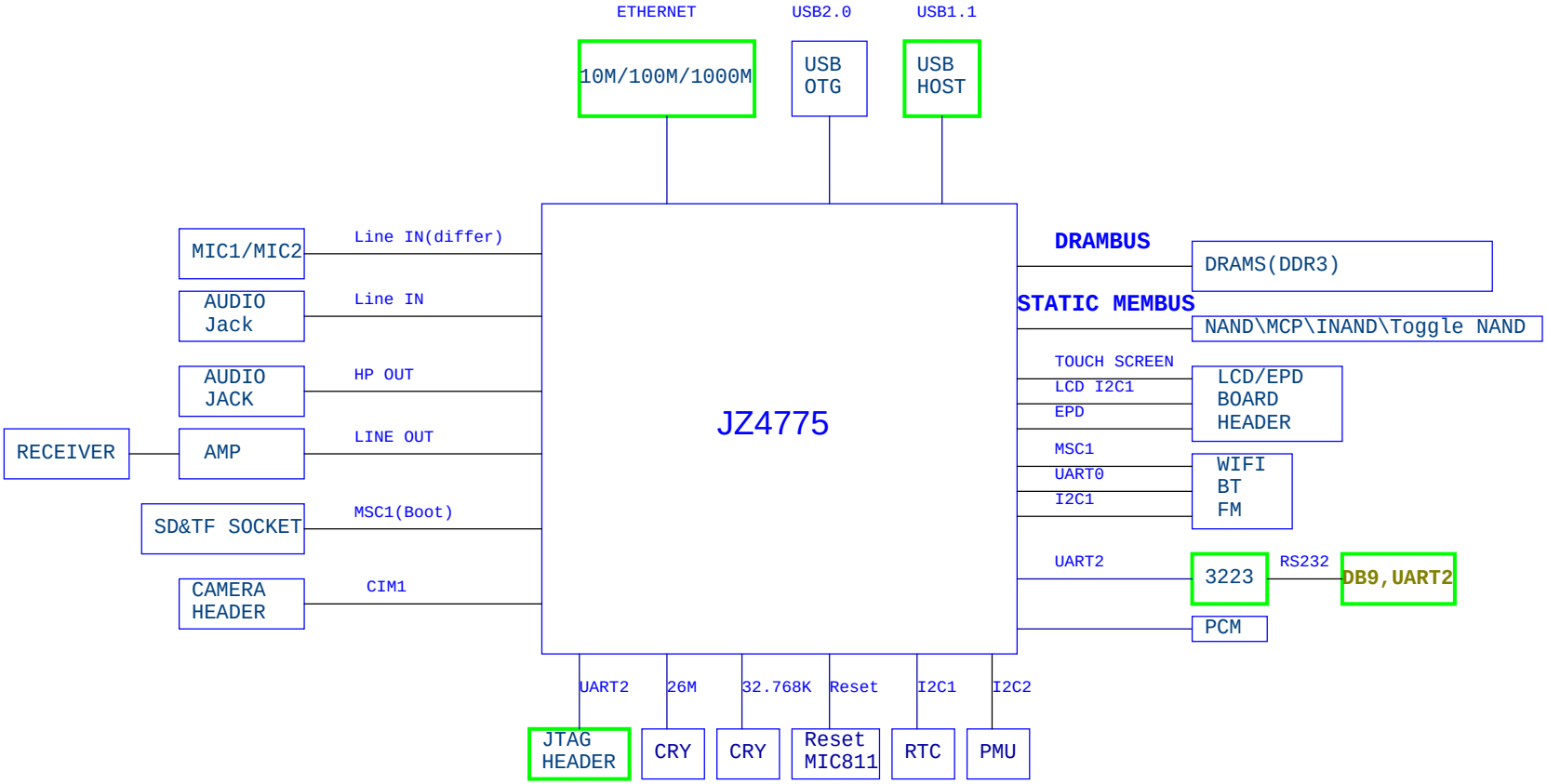
INGENIC

RD_JZ4775_MENSA_BOARD
Schematic Revision 1.0

INGENIC SEMICONDUCTOR CO.,LTD		
Title: RD_JZ4775_MENSA_BOARD		
Size: A3	Document Number: COVER	Rev: V1.0
Date: Tuesday, January 08, 2013	Sheet 1 of 14	



The green cloor is the debug board

INGENIC SEMICONDUCTOR CO.,LTD		
Title: RD_JZ4775_MENSA_BOARD		
Size: A3	Document Number: SYSTEM ARCHITECTURE	Rev: V1.0
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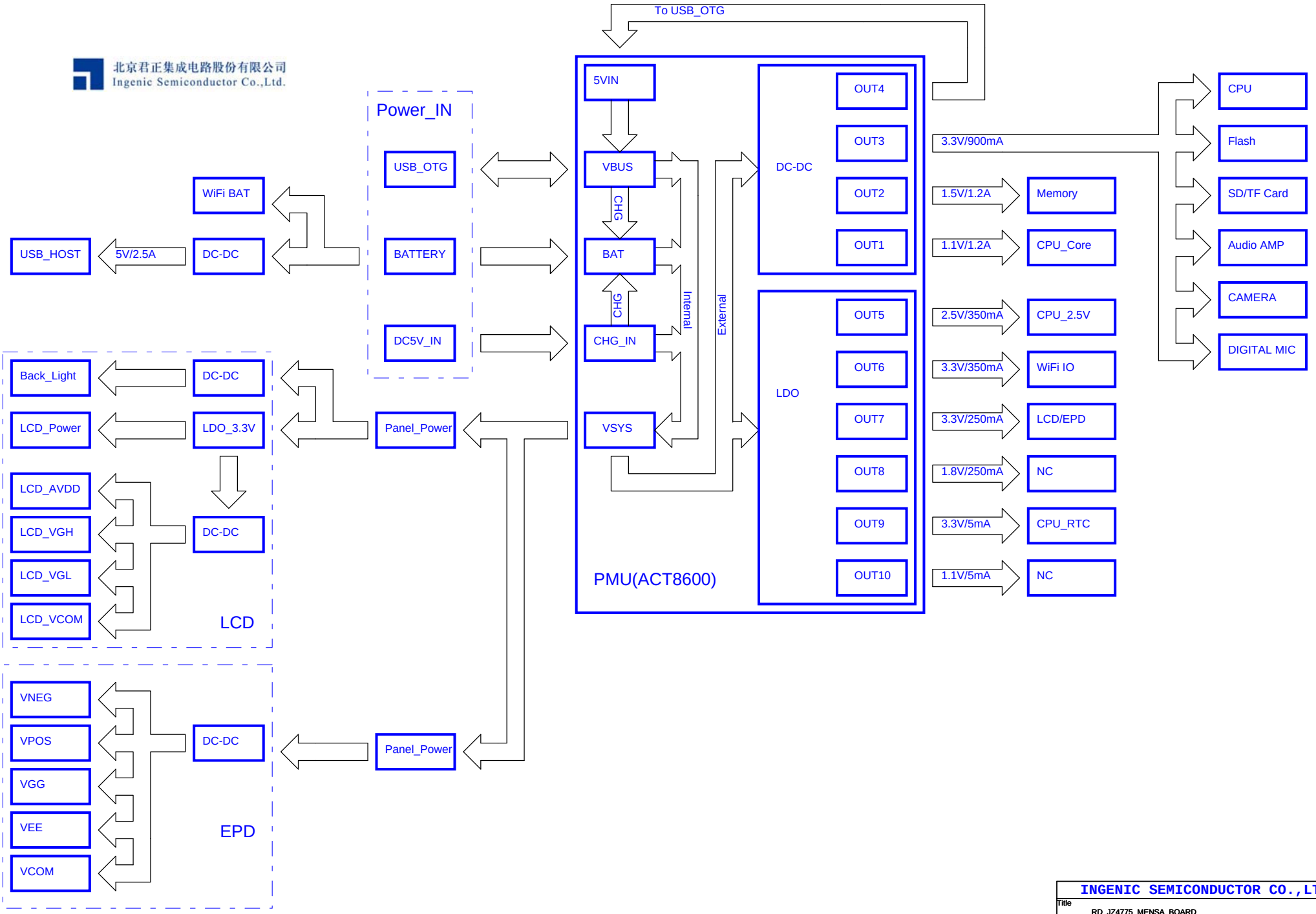
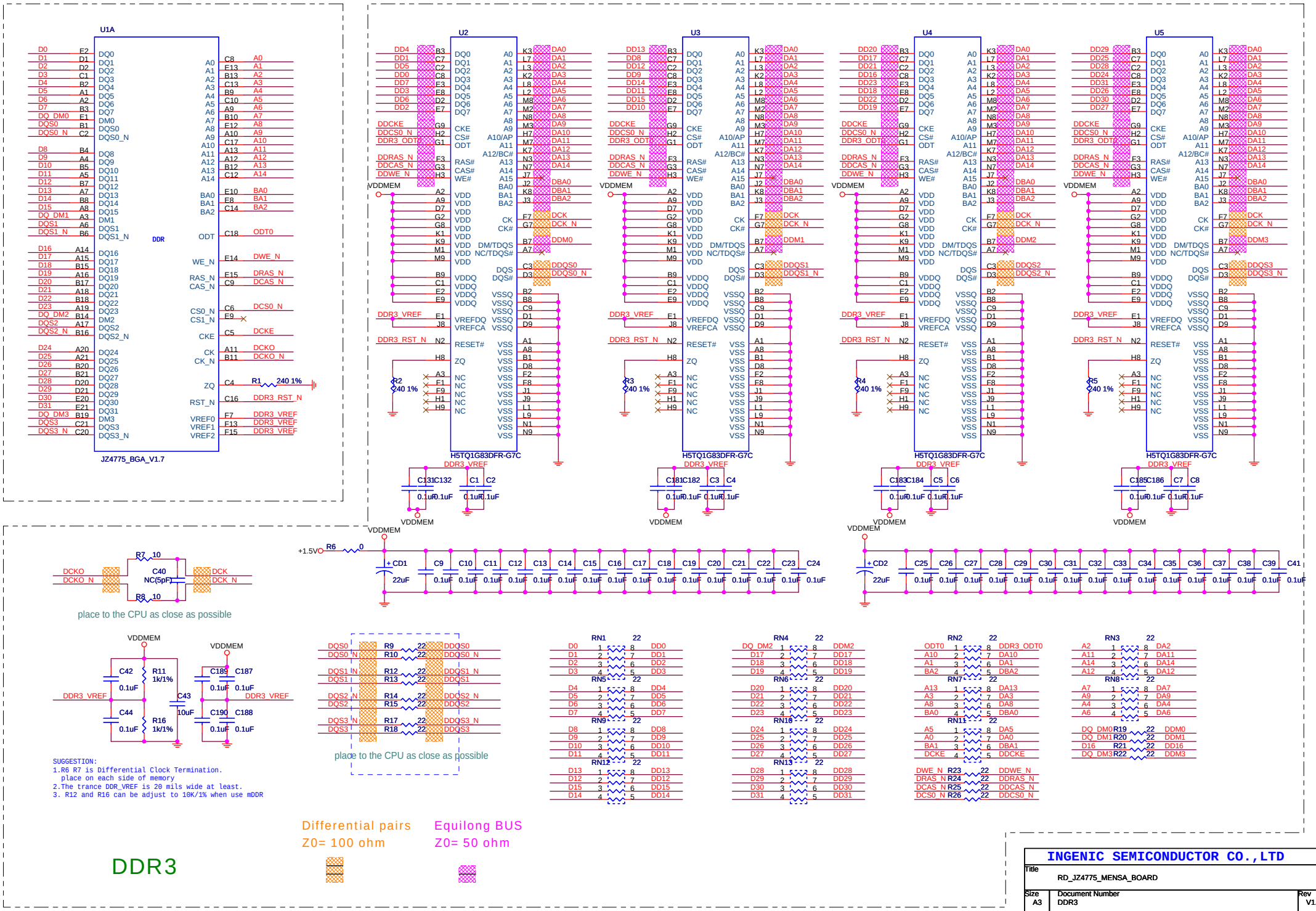


INGENIC SEMICONDUCTOR CO.,LTD		
Title: RD_JZ4775_MENSA_BOARD		
Size: A3	Document Number: POWER ARCHITECUTRE	Rev: V1.0
Date: Wednesday, January 16, 2013	Sheet 3 of 14	



INGENIC SEMICONDUCTOR CO.,LTD			
Title: RD_JZ4775_MENSA_BOARD			
Size: A3	Document Number: DDR3		Rev: V1.0
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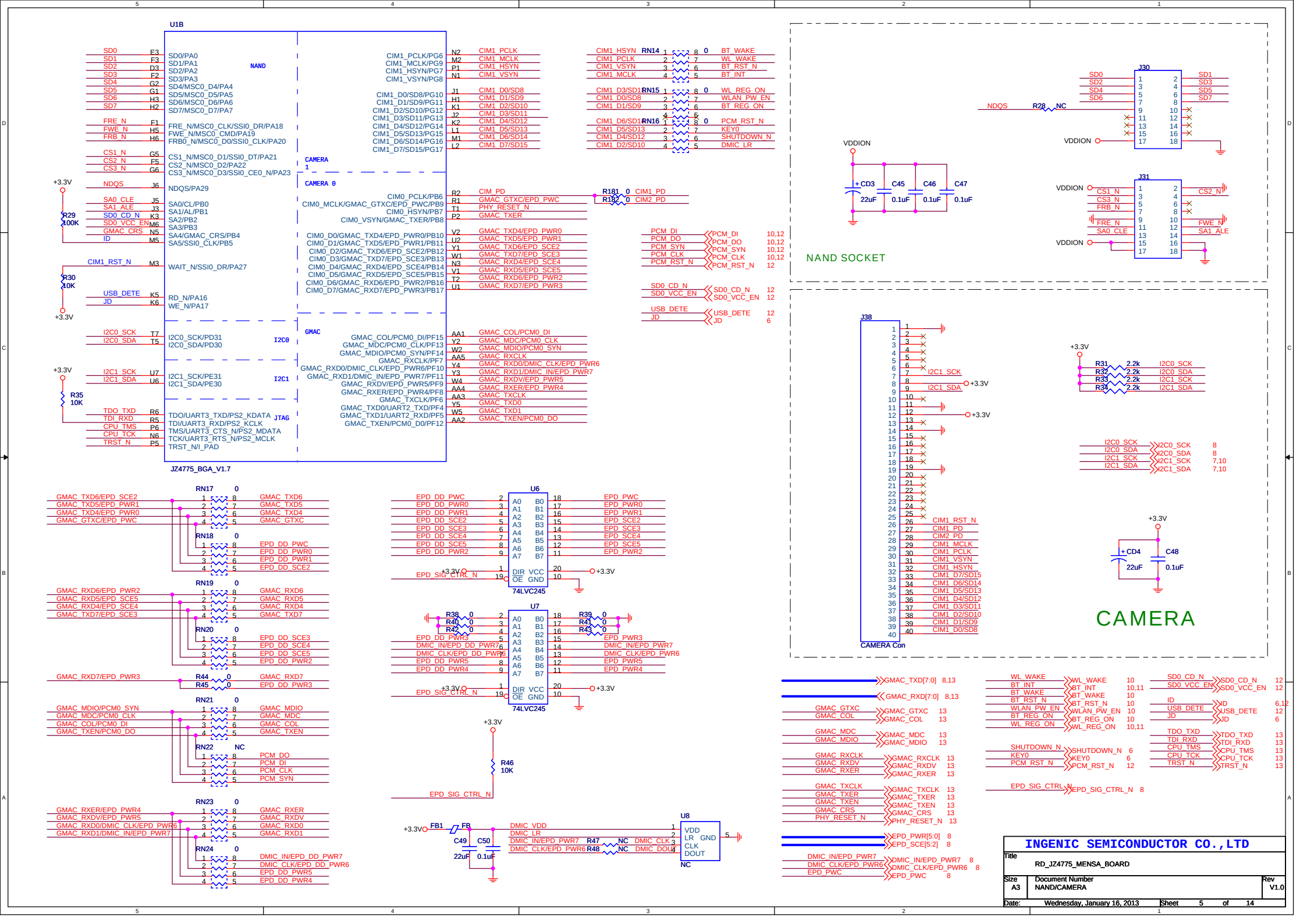

INGENIC SEMICONDUCTOR CO.,LTD	
Title	RD_JZ4775_MENSA_BOARD
Size A3	Document Number NAND/CAMERA
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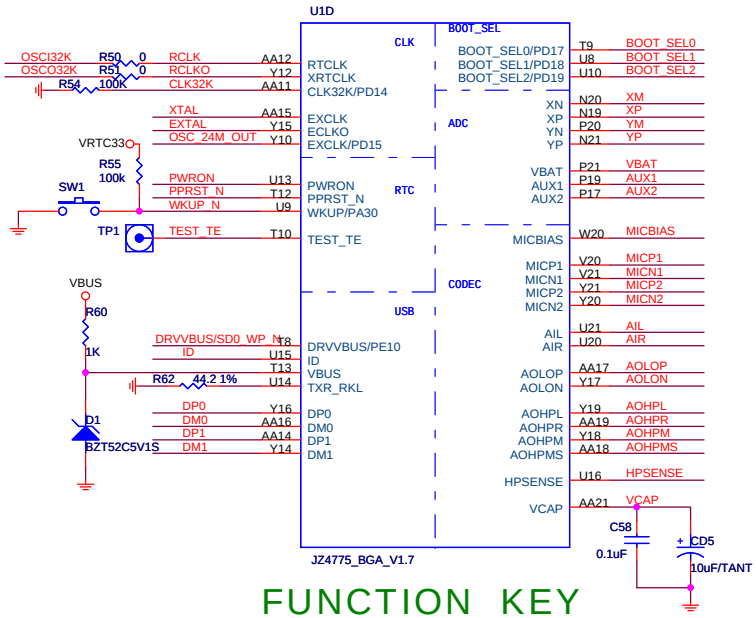
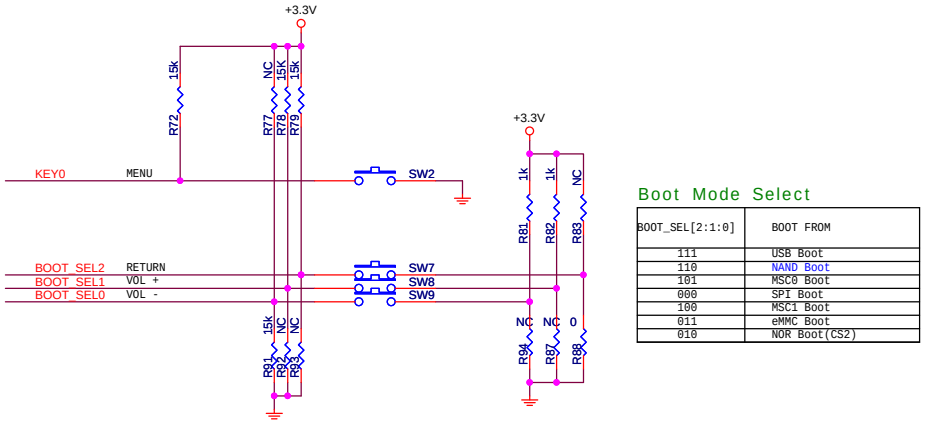
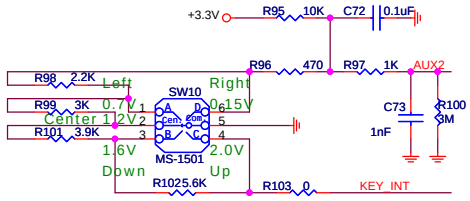


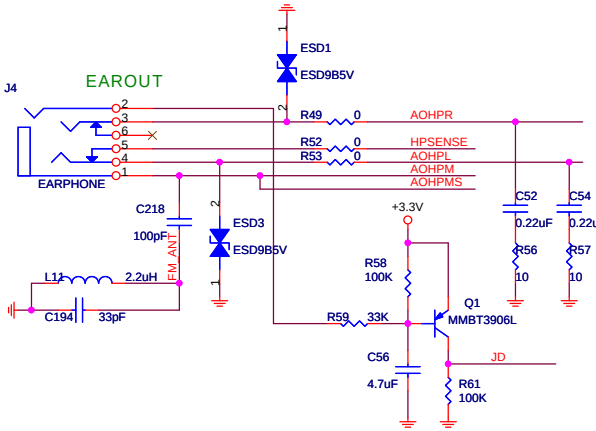
FUNCTION KEY



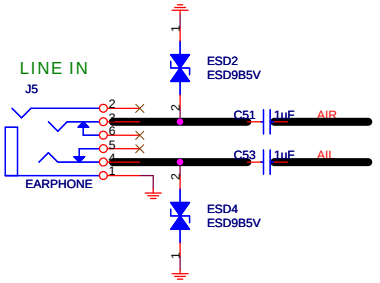
Boot Mode Select

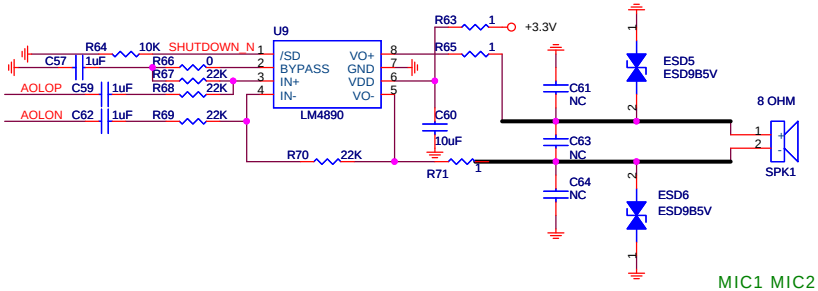
BOOT_SEL[2:1:0]	BOOT FROM
111	USB Boot
110	NAND Boot
101	MSC0 Boot
000	SPI Boot
100	MSC1 Boot
011	eMMC Boot
010	NOR Boot(CS2)

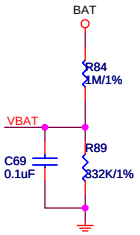


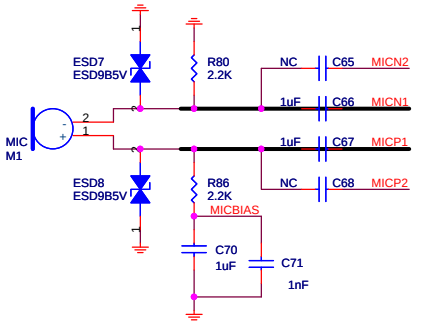


The AOHPMS and AOHPM must be connected at terminate, near the headphone Jack









AUDIO

RCLK 7
CLK32K 10
XTAL 7
EXTAL 7
OSCI32K 7
OSCO32K 7
DP0 12
DM0 12
DP1 13
DM1 13
DRVVBUS 9
ID 5,12
KEY0 5

AIL 10,11
AIR 10,11
XM 8
XP 8
YM 8
YP 8
SD0_WP_N 12
AUX1 8
FM_ANT 10,11

OSC_24M_OUT 10
PWRON 9
PPRST_N 7,9
SHUTDOWN_N 5
JD 5

INGENIC SEMICONDUCTOR CO.,LTD	
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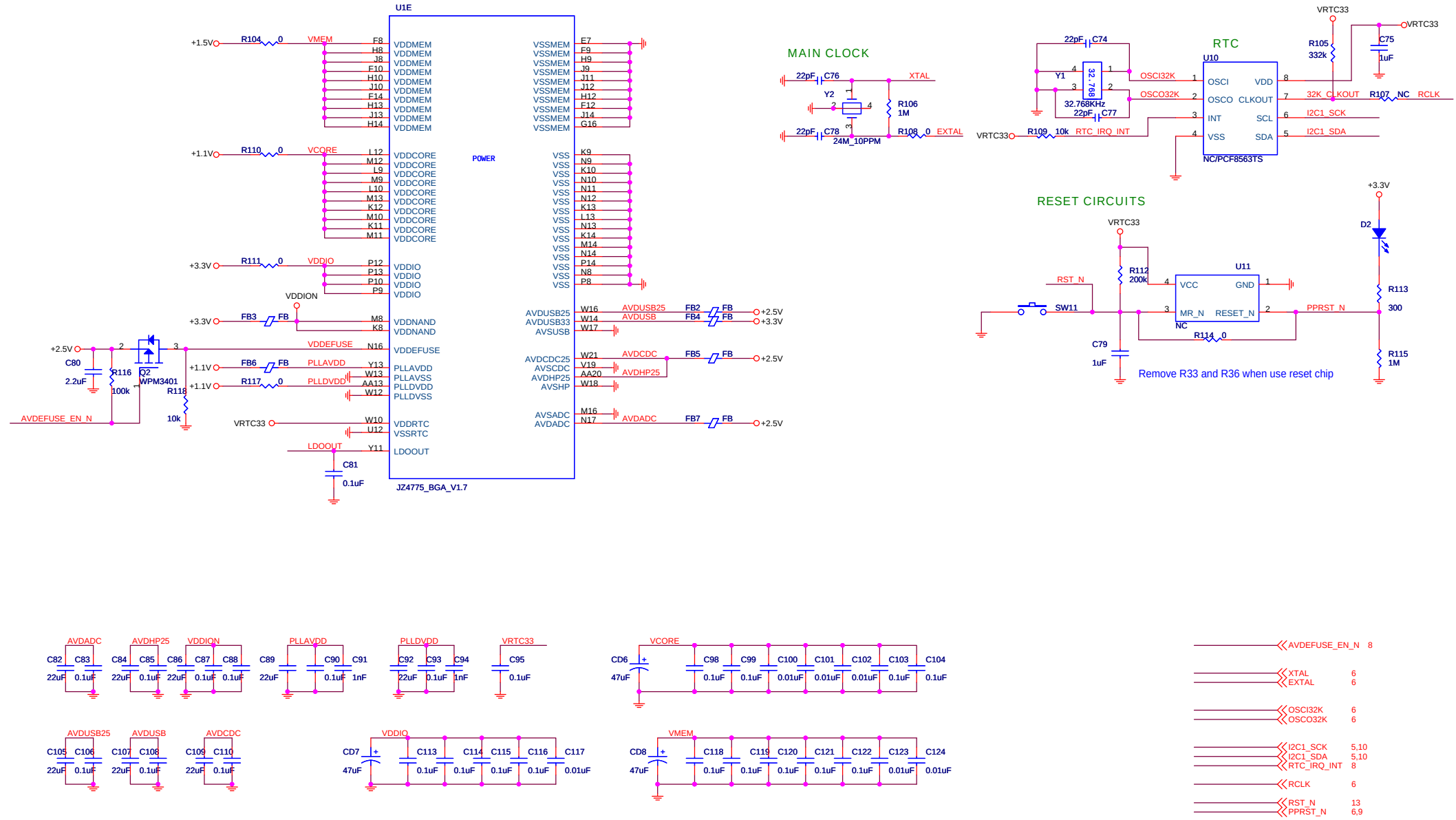

INGENIC SEMICONDUCTOR CO.,LTD		
Title: RD_JZ4775_MENSA_BOARD		
Size: A3	Document Number: RTC	Rev: V1.0
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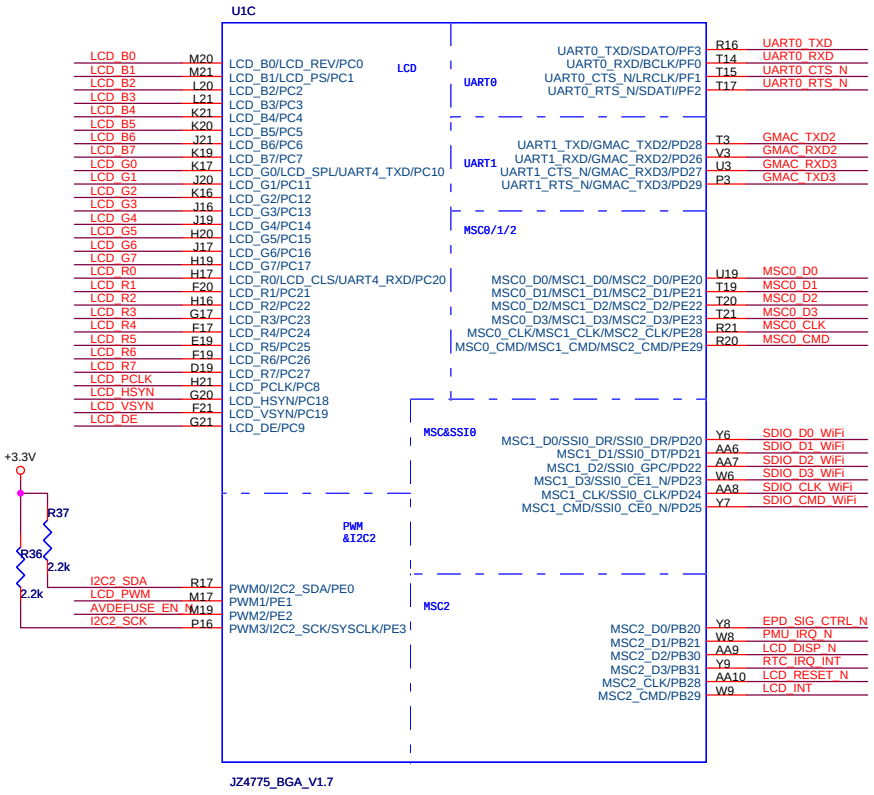

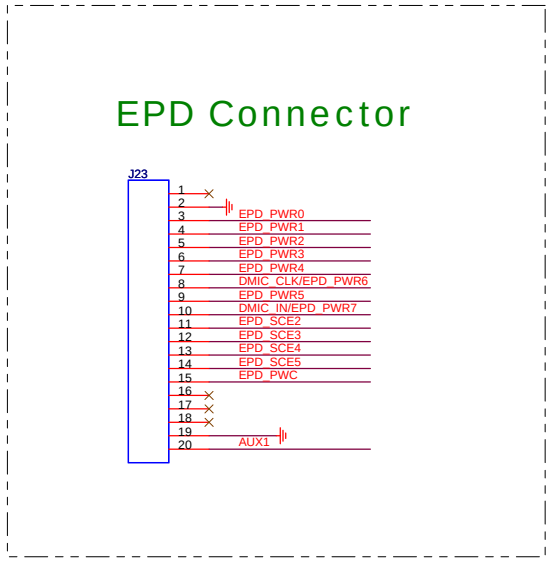

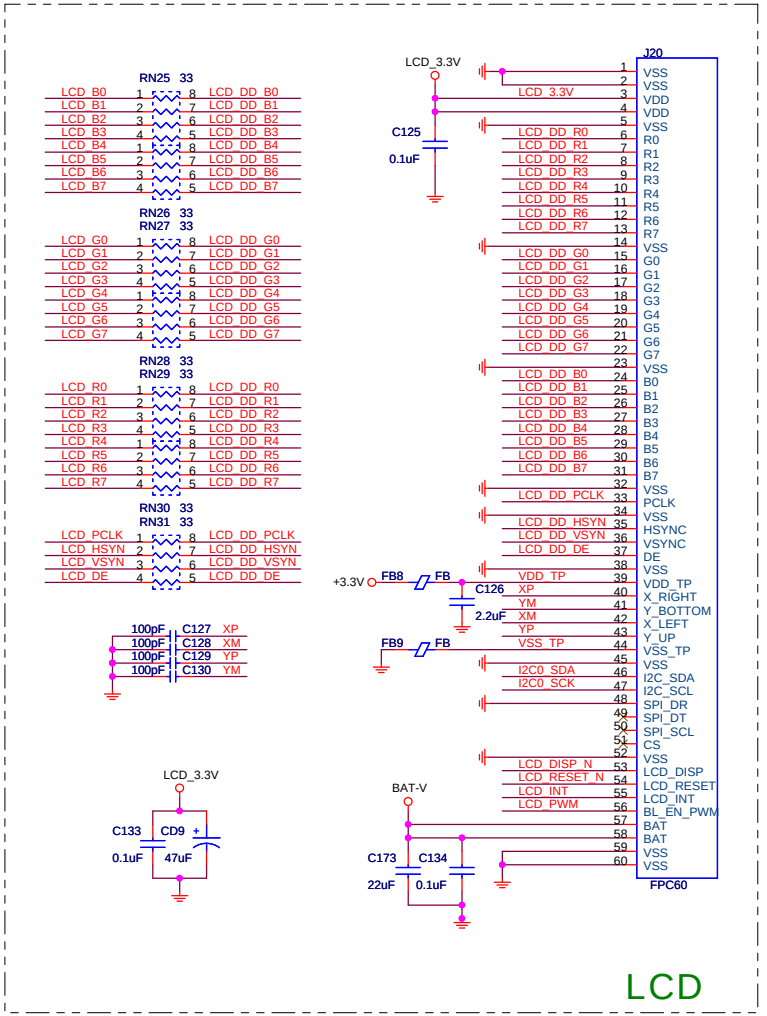

EPD_PWR[5:0] 5
EPD_SCE[5:2] 5
DMIC_IN/EPD_PWR7 DMIC_IN/EPD_PWR7 5
DMIC_CLK/EPD_PWR6 DMIC_CLK/EPD_PWR6 5
EPD_PWC EPD_PWC 5

MSC0_D0 MSC0_D0 12
MSC0_D1 MSC0_D1 12
MSC0_D2 MSC0_D2 12
MSC0_D3 MSC0_D3 12
MSC0_CLK MSC0_CLK 12
MSC0_CMD MSC0_CMD 12

UART0_TXD UART0_TXD 10,11
UART0_RXD UART0_RXD 10,11
UART0_CTS_N UART0_CTS_N 10,11
UART0_RTS_N UART0_RTS_N 10,11

XM XM 6
XP XP 6
YM YM 6
YP YP 6

AUX1 AUX1 6

SDIO_D0_WiFi SDIO_D0_WiFi 10,11
SDIO_D1_WiFi SDIO_D1_WiFi 10,11
SDIO_D2_WiFi SDIO_D2_WiFi 10,11
SDIO_D3_WiFi SDIO_D3_WiFi 10,11
SDIO_CLK_WiFi SDIO_CLK_WiFi 10
SDIO_CMD_WiFi SDIO_CMD_WiFi 10,11

GMAC_TXD2 GMAC_TXD2 13
GMAC_RXD2 GMAC_RXD2 13
GMAC_RXD3 GMAC_RXD3 13
GMAC_TXD3 GMAC_TXD3 13

I2C0_SDA I2C0_SDA 5
I2C0_SCK I2C0_SCK 5

RTC_IRQ_INT RTC_IRQ_INT 7

I2C2_SDA I2C2_SDA 9
I2C2_SCK I2C2_SCK 9

AVDEFUSE_EN_N AVDEFUSE_EN_N 7

EPD_SIG_CTRL_N EPD_SIG_CTRL_N 5
PMU_IRQ_N PMU_IRQ_N 9

INGENIC SEMICONDUCTOR CO.,LTD	
Title	RD_JZ4775_MENSA_BOARD
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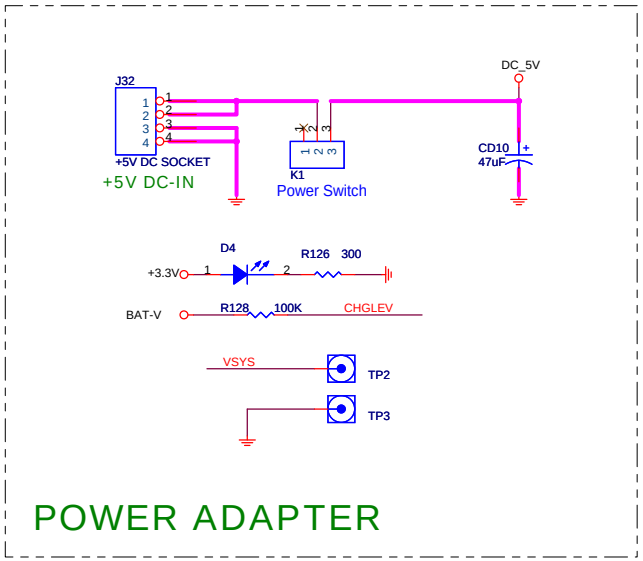

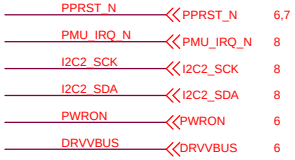

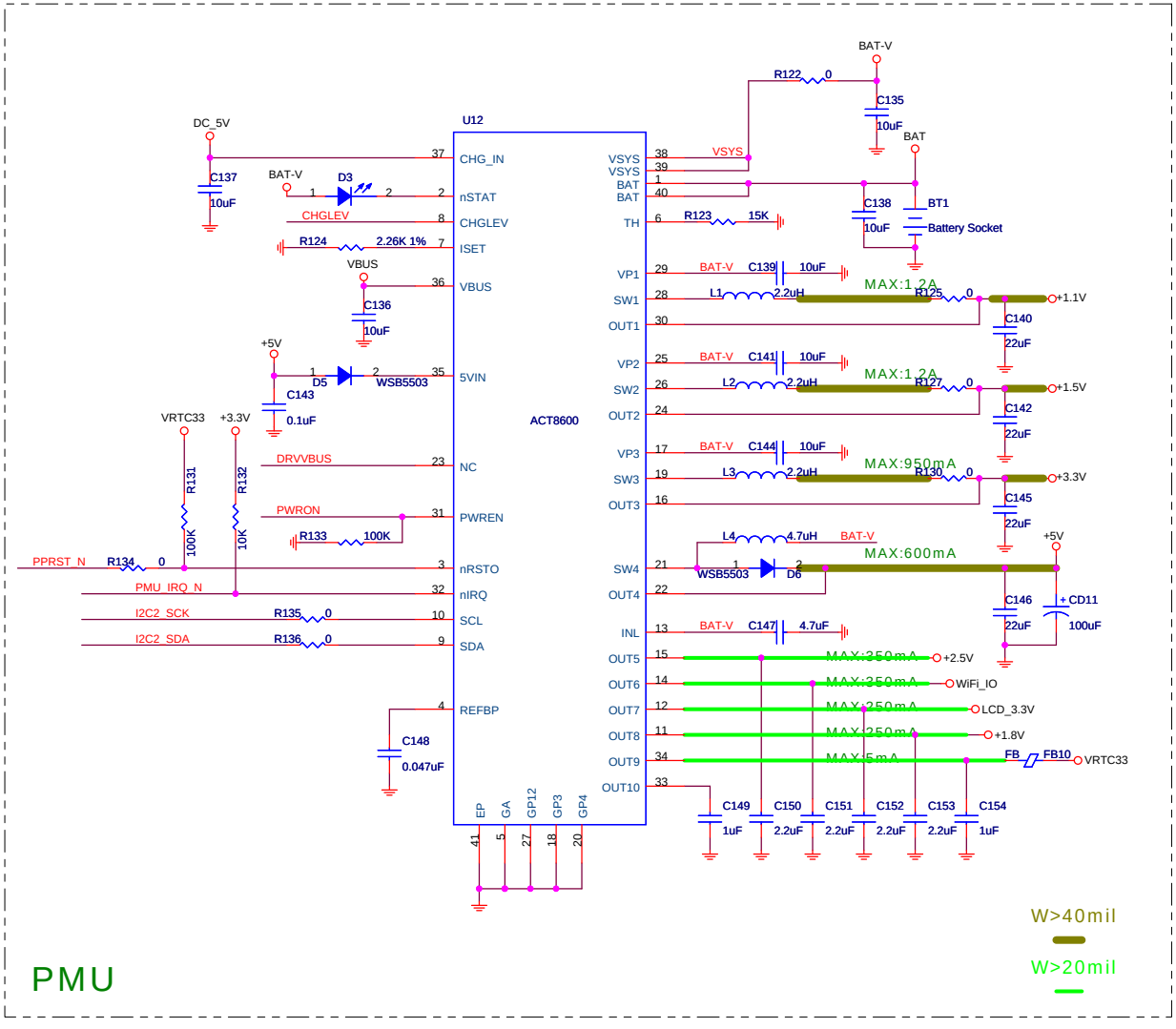

INGENIC SEMICONDUCTOR CO.,LTD		
Title: RD_JZ4775_MENSA_BOARD		
Size: A3	Document Number: PMU	Rev: V1.0
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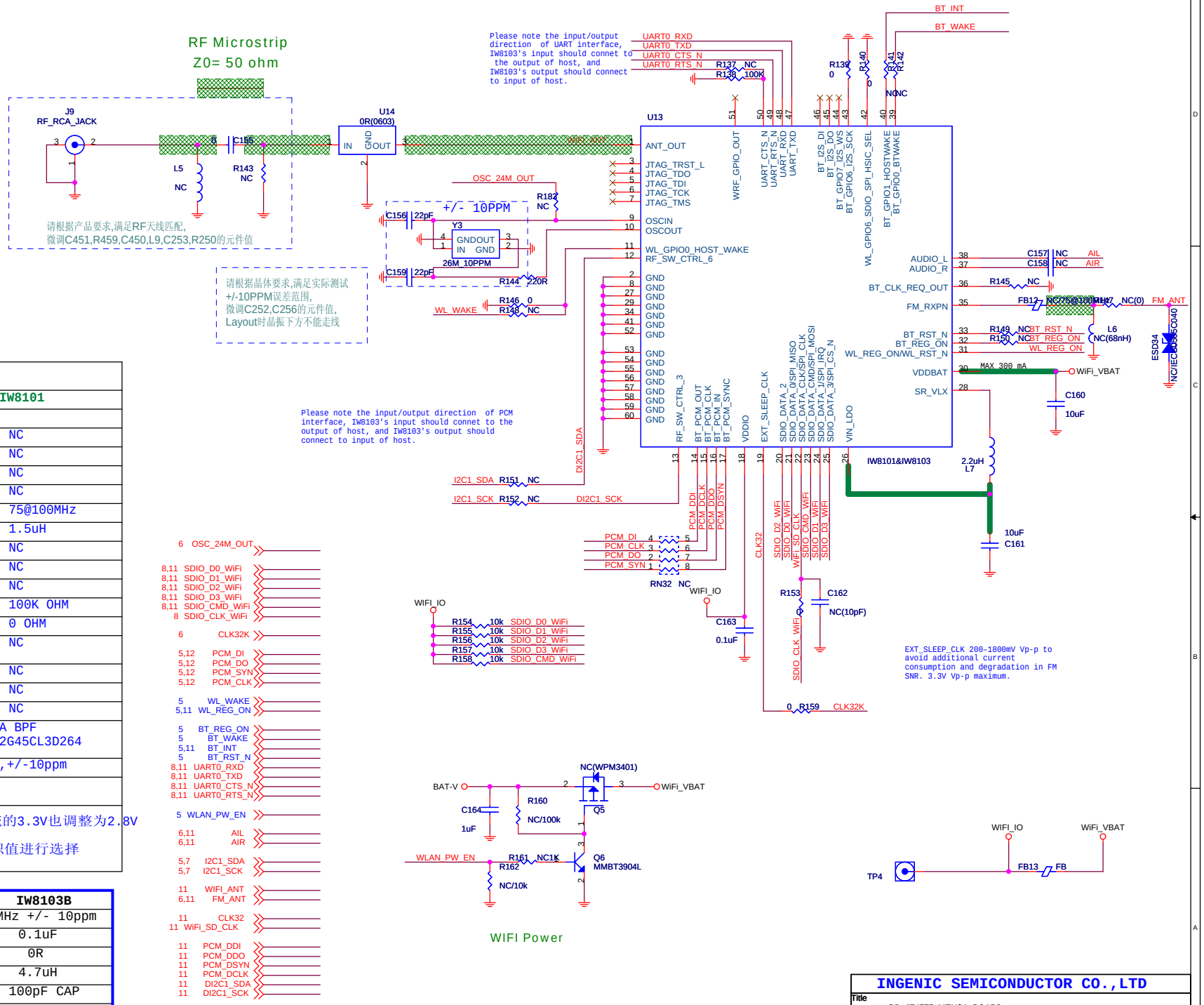

Component select in IW8101 & IW8103

Part Number	IW8103	IW8101
	value	
C157,C158	1uF	NC
C164	1uF	NC
ESD34	TESB0R15V05B1X	NC
FB12	75@100MHz	NC
FB13	NC	75@100MHz
L7	2.2uH	1.5uH
Q5	WPM3401	NC
Q6	MMBT3904L	NC
RN32	0 OHM	NC
R138	NC	100K OHM
R139,R146	NC	0 OHM
R137,R141,R142,R147 R148,R149,R150	0 OHM	NC
R160	100K OHM	NC
R162	10K OHM	NC
R161	1K OHM	NC
U14	0 OHM	MURATA BPF LFB182G45CL3D264
Y3	37.4MHz,+/-10ppm	26MHz,+/-10ppm

备注

使用IW8103，WIFI_IO电压调整为2.8V,同时系统的3.3V也调整为2.8V

清单内无标识的物料为共用料，请按原理图标识值进行选择

器件编号	IW8103	IW8103B
Y3	37.4MHz +/- 10ppm	26MHz +/- 10ppm
C157,C158	1uF	0.1uF
R151,R152	NC	0R
L7	2.2uH	4.7uH
FB12	75@100MHz Bead	100pF CAP
R140,R142	0R	NC

INGENIC SEMICONDUCTOR CO.,LTD		
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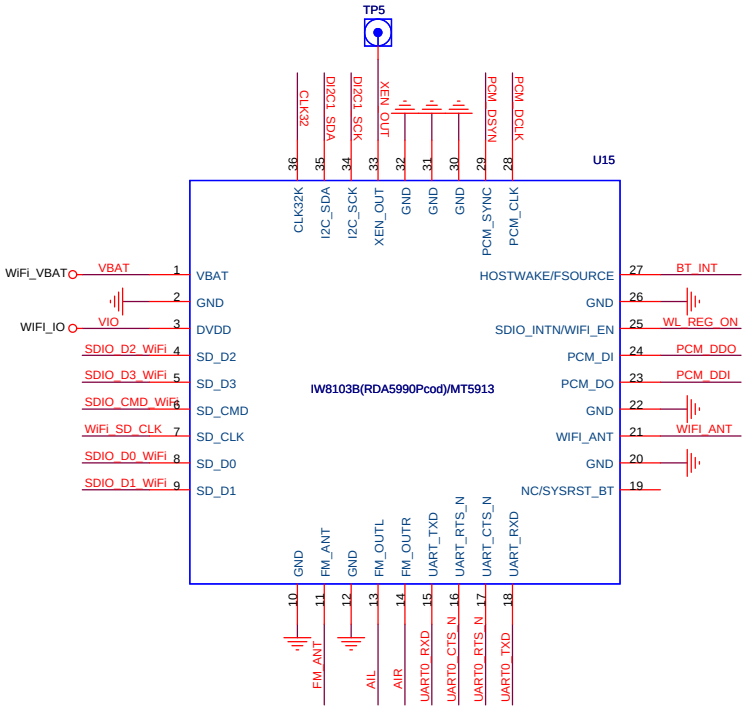

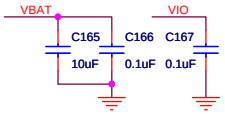

8,10 SDIO_D0_WiFi
8,10 SDIO_D1_WiFi
8,10 SDIO_D2_WiFi
8,10 SDIO_D3_WiFi
8,10 SDIO_CMD_WiFi
10 WiFi_SD_CLK

10 CLK32

5,10 WL_REG_ON
5,10 BT_INT

8,10 UART0_RXD
8,10 UART0_TXD
8,10 UART0_CTS_N
8,10 UART0_RTS_N

6,10 AIL
6,10 AIR

10 WIFI_ANT
6,10 FM_ANT
10 PCM_DDI
10 PCM_DDO
10 PCM_DSYN
10 PCM_DCLK
10 DI2C1_SDA
10 DI2C1_SCK

INGENIC SEMICONDUCTOR CO.,LTD		
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Size: A3	Document Number: WIFI_IW8103B/MT5913	Rev: V1.0
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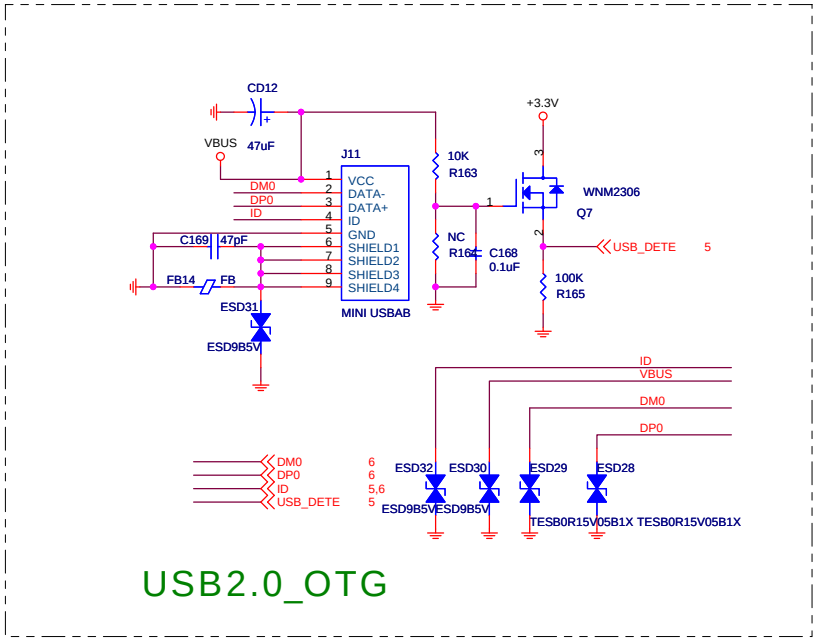

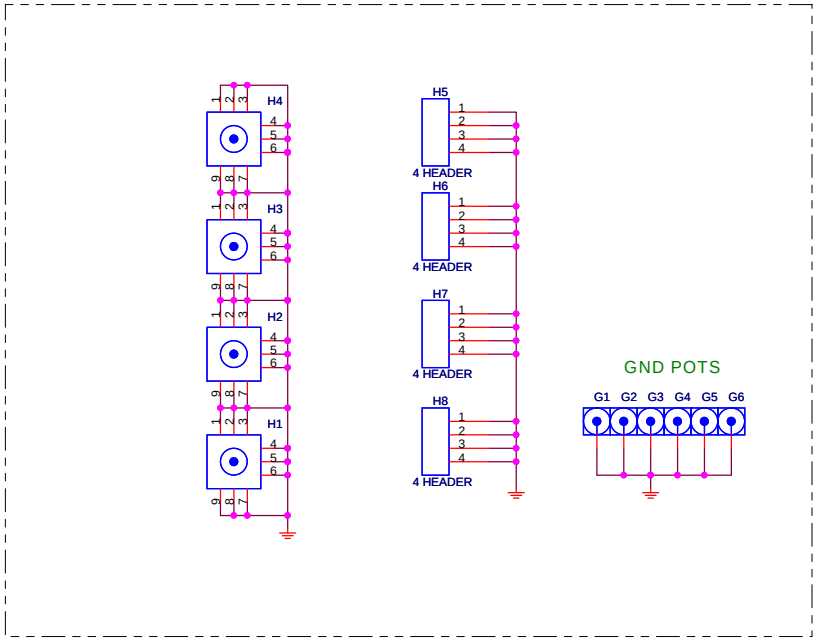

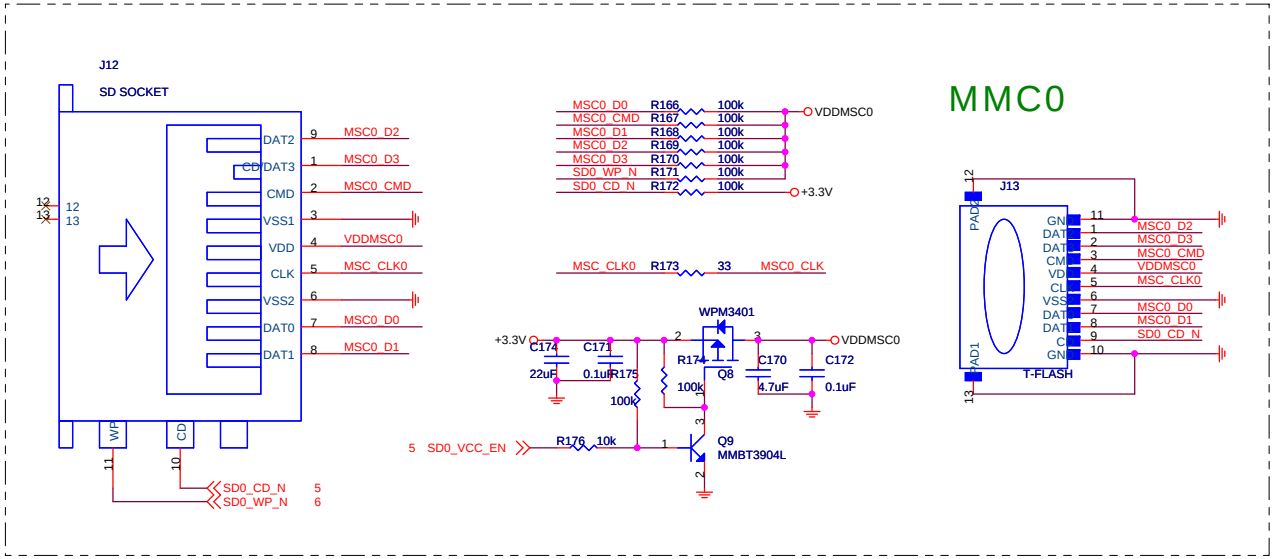

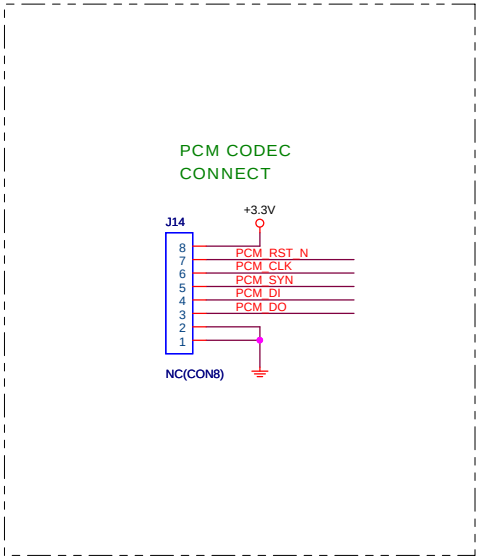

PCM_DI — PCM_DI 5,10
PCM_DO — PCM_DO 5,10
PCM_SYN — PCM_SYN 5,10
PCM_CLK — PCM_CLK 5,10
PCM_RST_N — PCM_RST_N 5

MSC0_D0 — MSC0_D0 8
MSC0_D1 — MSC0_D1 8
MSC0_D2 — MSC0_D2 8
MSC0_D3 — MSC0_D3 8
MSC0_CLK — MSC0_CLK 8
MSC0_CMD — MSC0_CMD 8
SD0_CD_N — SD0_CD_N 5
SD0_WP_N — SD0_WP_N 6
SD0_VCC_EN — SD0_VCC_EN 5

INGENIC SEMICONDUCTOR CO.,LTD		
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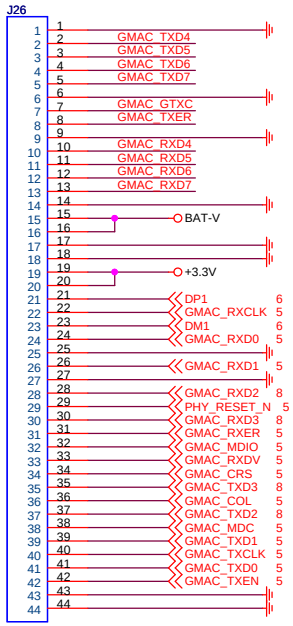
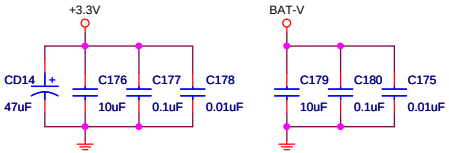
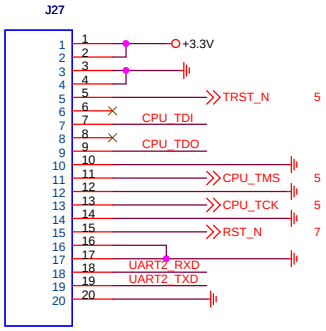
Ethernet & USB Con

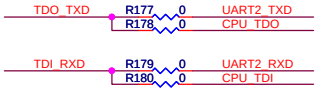


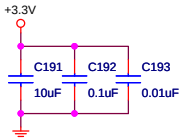
Ethernet & USB Con

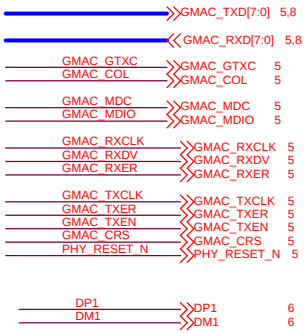


JDI & Uart Con









INGENIC SEMICONDUCTOR CO.,LTD		
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Data	Revision	Change
Jan 8 2013	Rev1.0	1. First Revision

INGENIC SEMICONDUCTOR CO.,LTD		
Title: RD_JZ4775_MENSA_BOARD		
Size: A3	Document Number: REVISION HISTORY	Rev: V1.0
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