



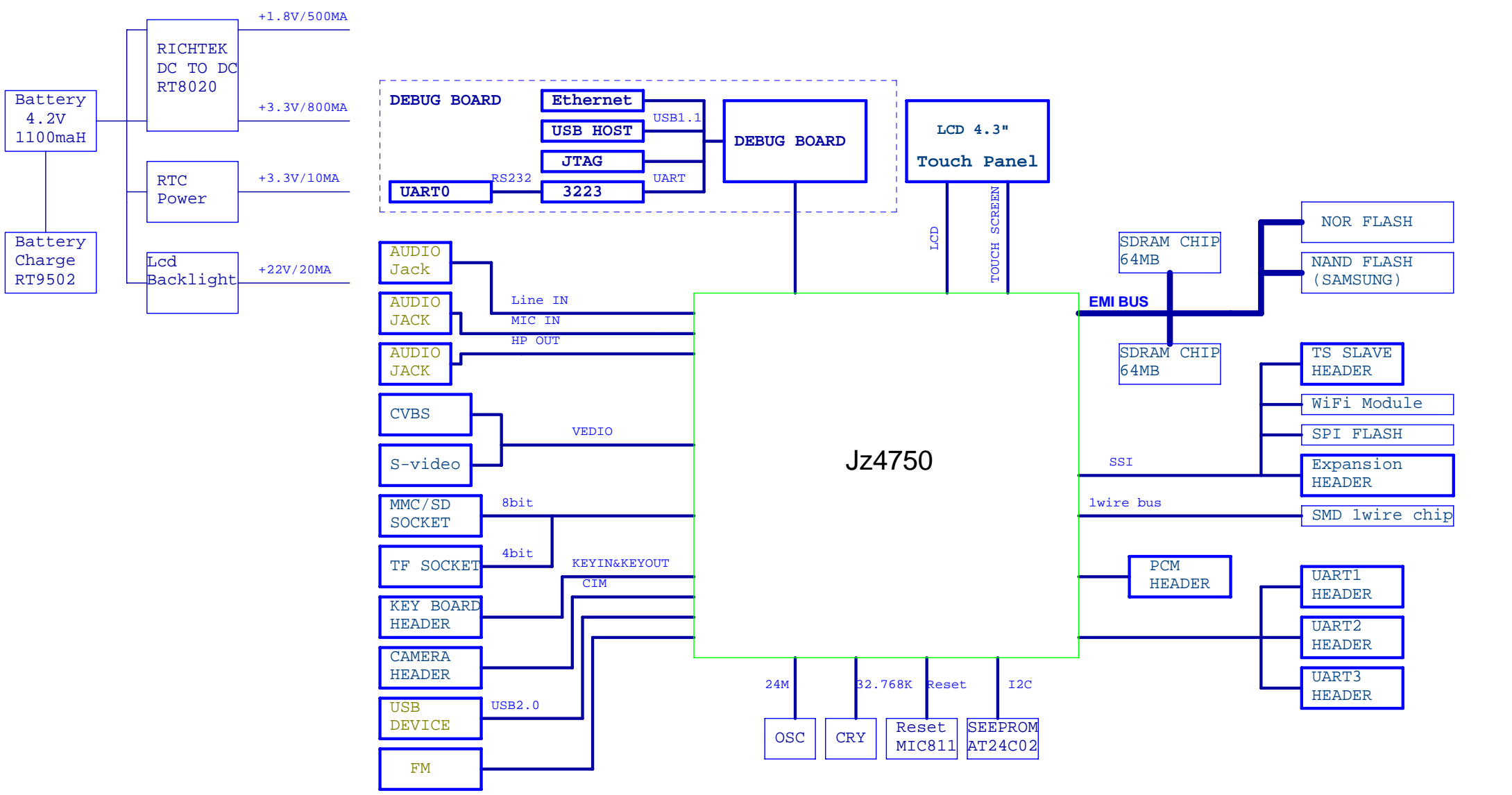
RD4750 APUS Board Schematic Revision 1.2

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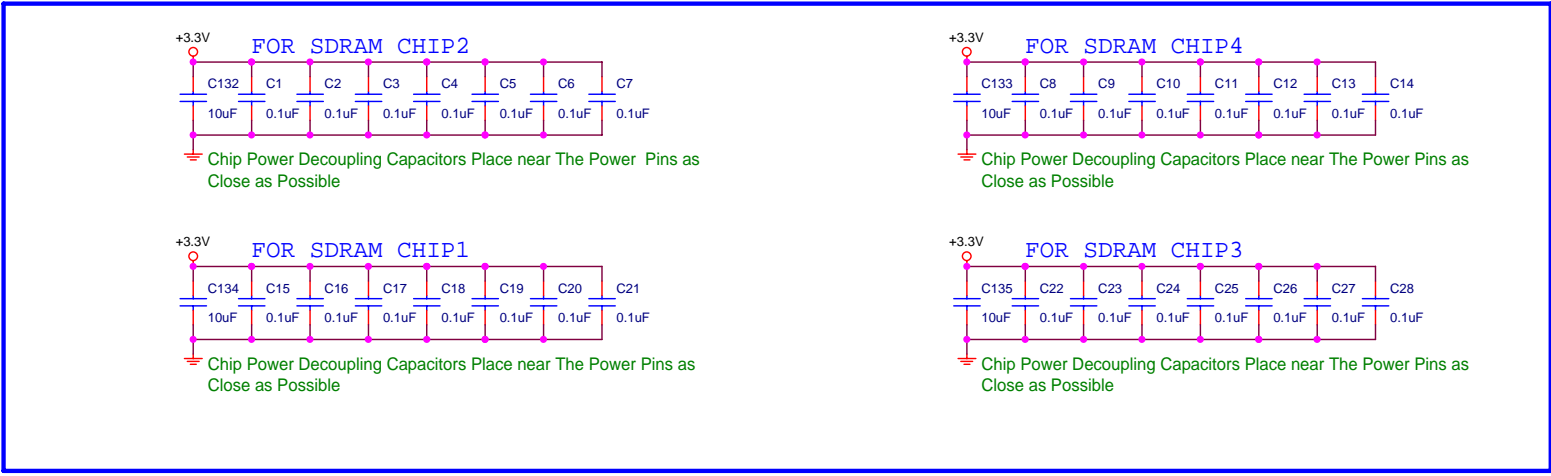
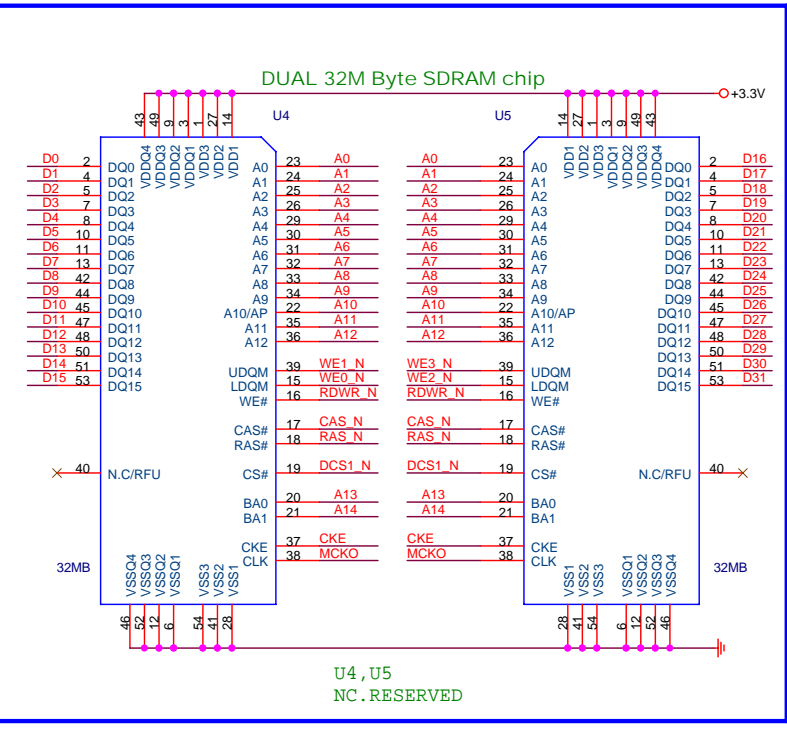
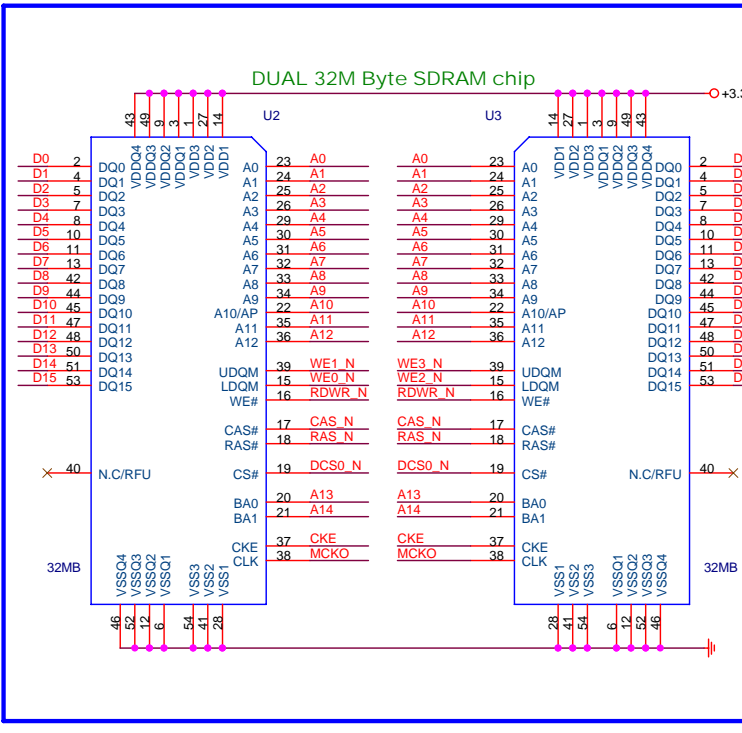
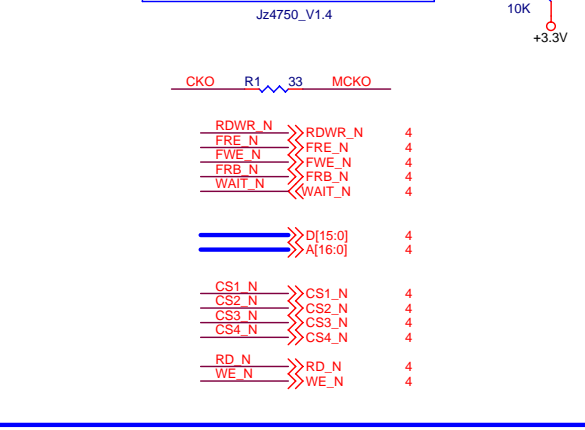
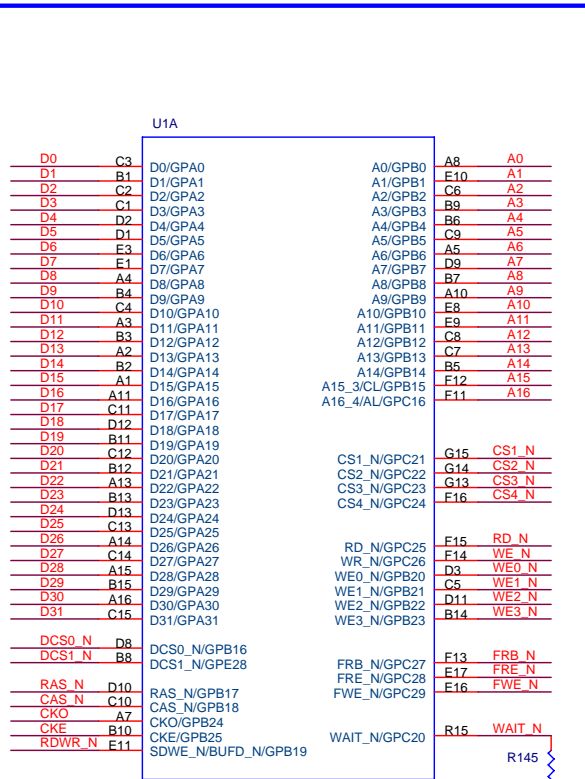
INGENIC Semiconductor Corporation
System Technology Department

Release Data:
2009.6.2

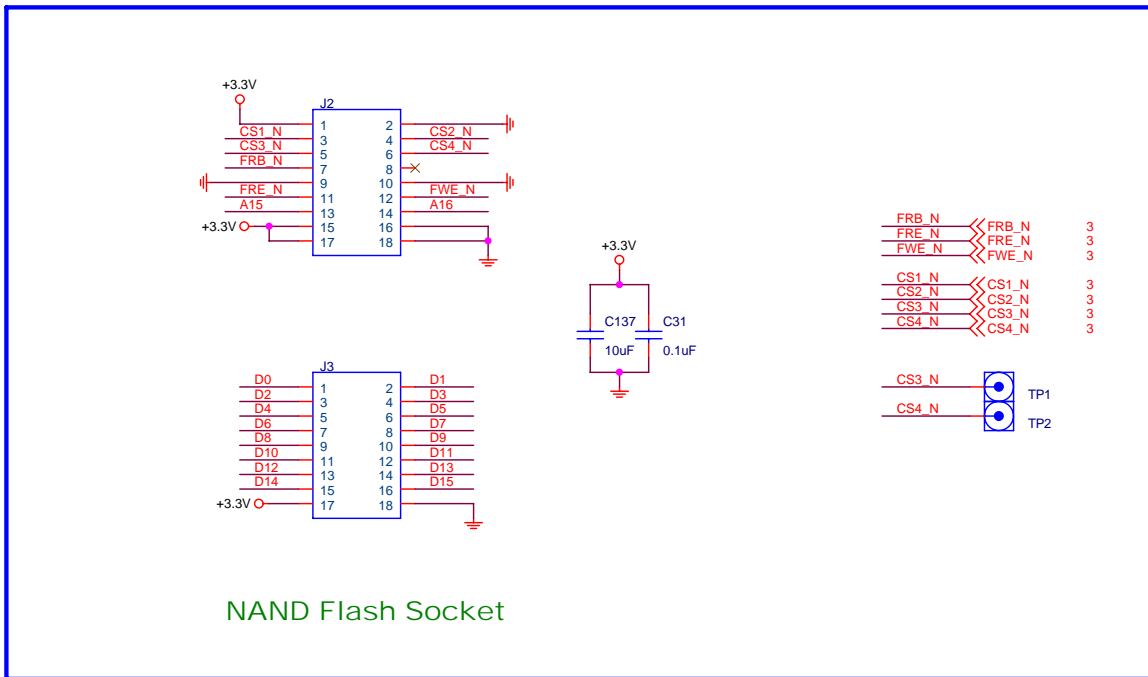
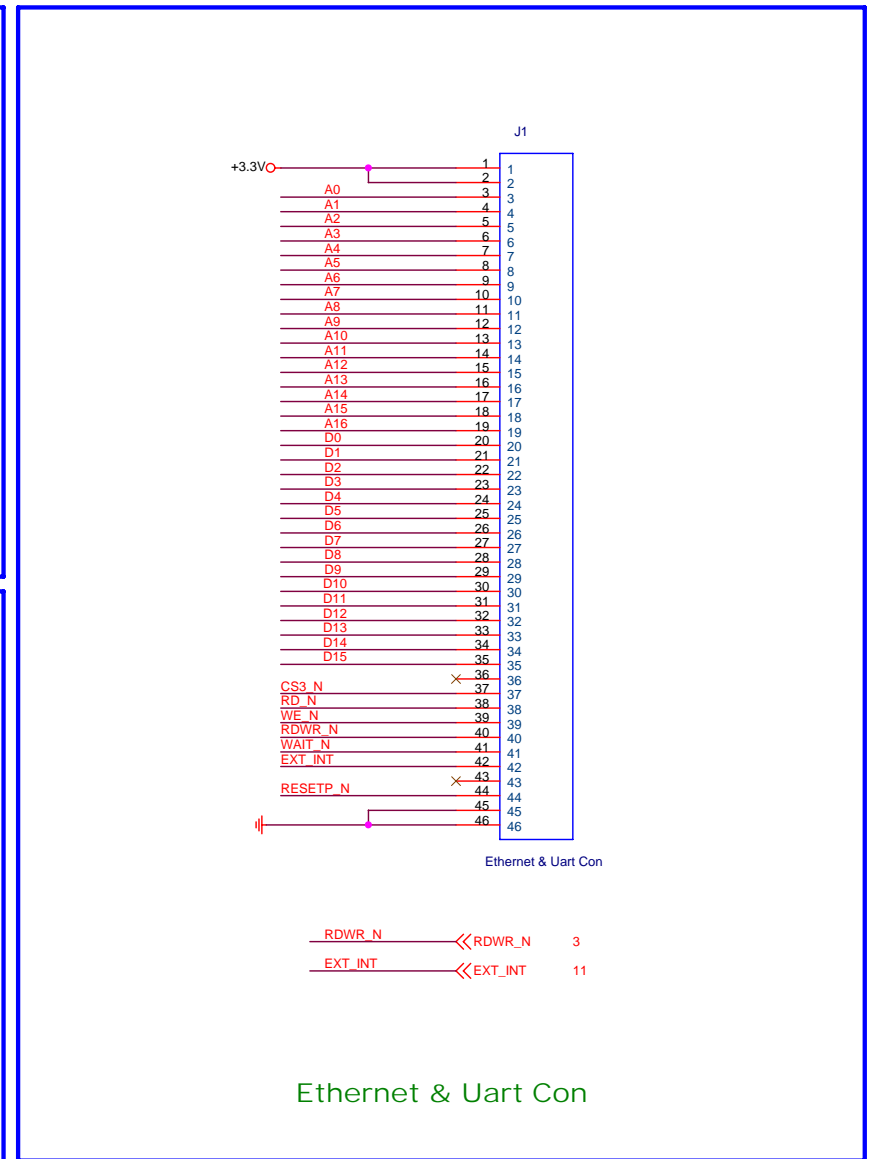
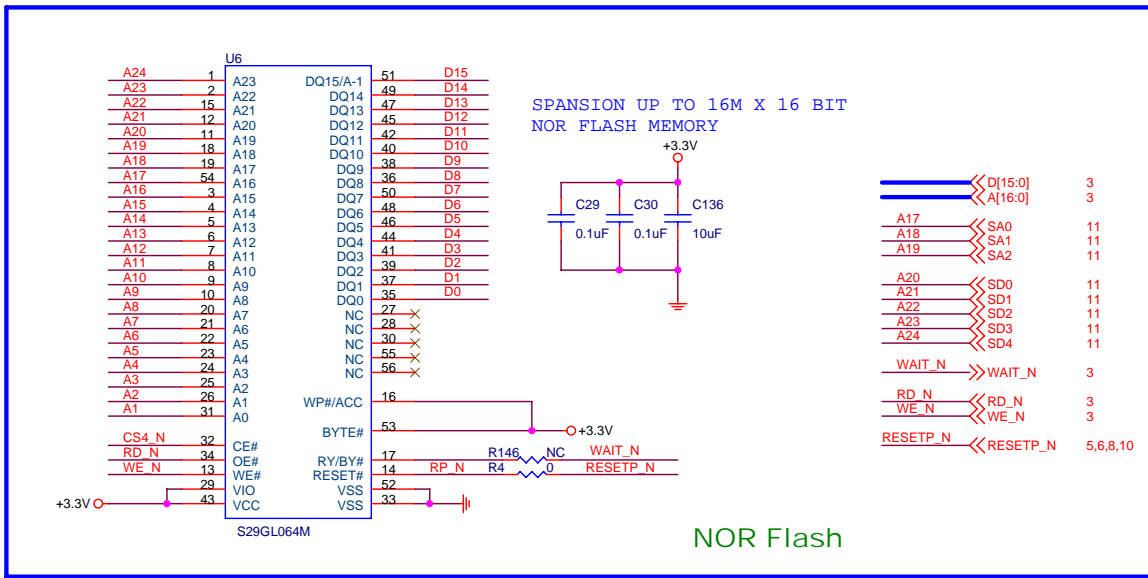
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B	Cover	1.2
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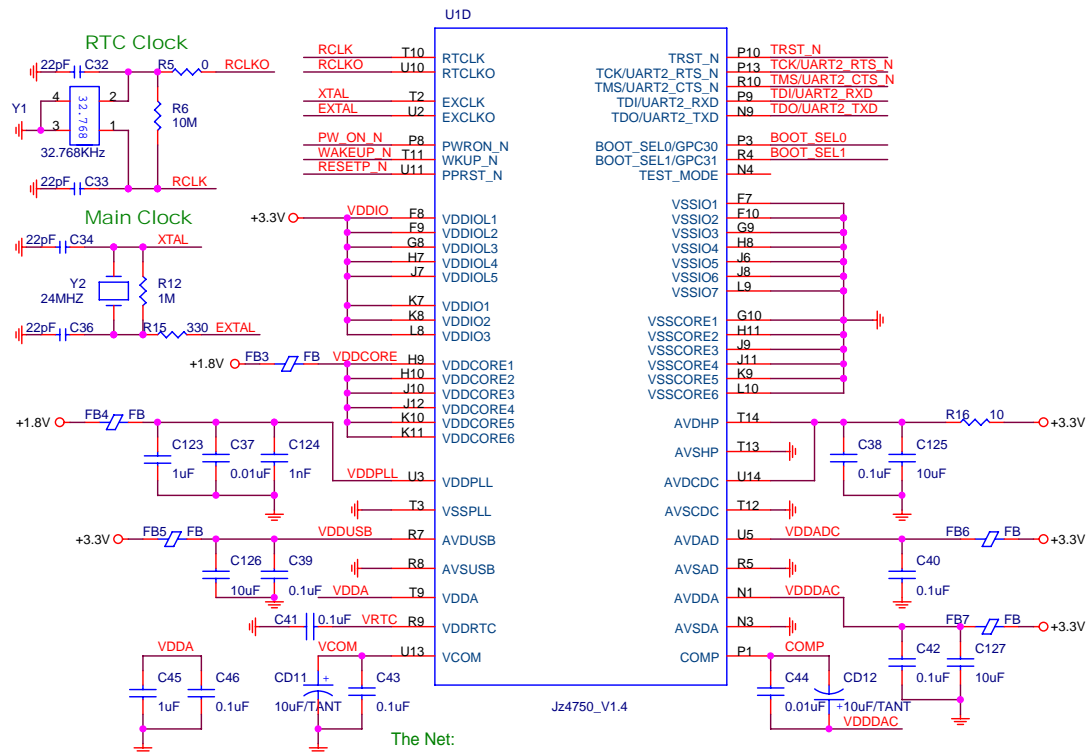


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B	System Architecture	1.2
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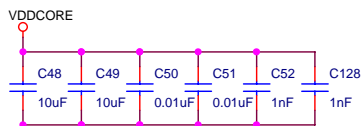


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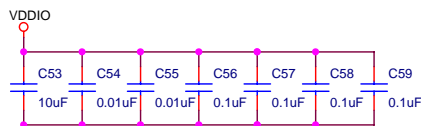




The Net:
 EXTAL, XTAL, RCLKO, RCLK, VDDPLL, VDDUSB, VDDA,
 VCOM, VRTC, COMP, VDDDAC, VDDADC, AVDCDC,
 AVDHP .should be as short and wide as possible.
 The capacitor:
 should be placed close to CPU Pin.



Core Power Decoupling Capacitors
 Place near The Power Pins as
 Close as Possible



IO Power Decoupling Capacitors
 Place near The Power Pins as
 Close as Possible

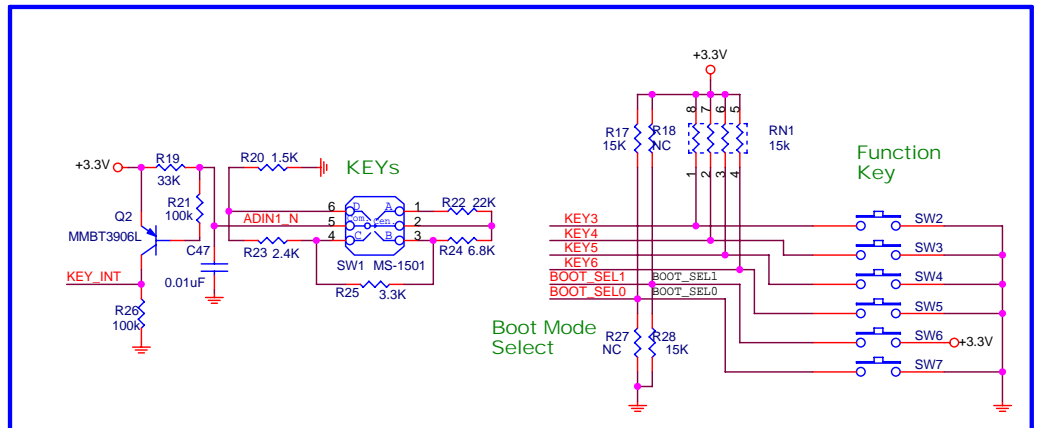
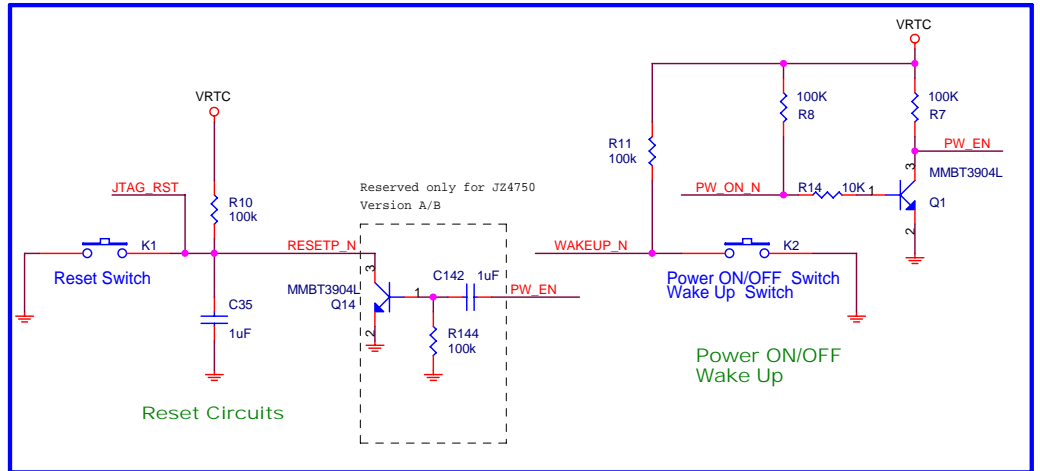
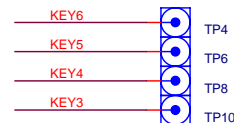
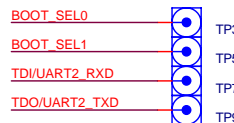
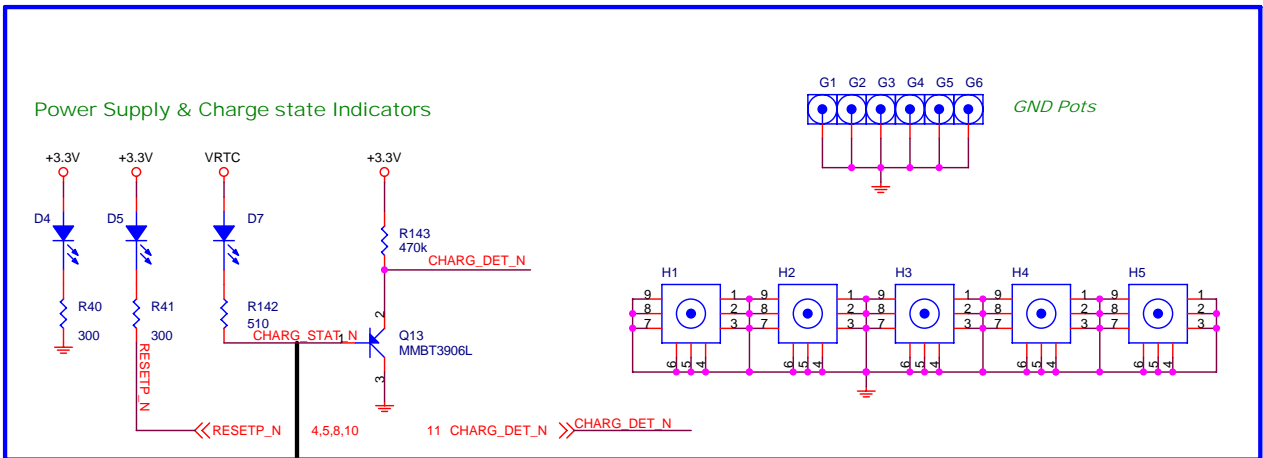
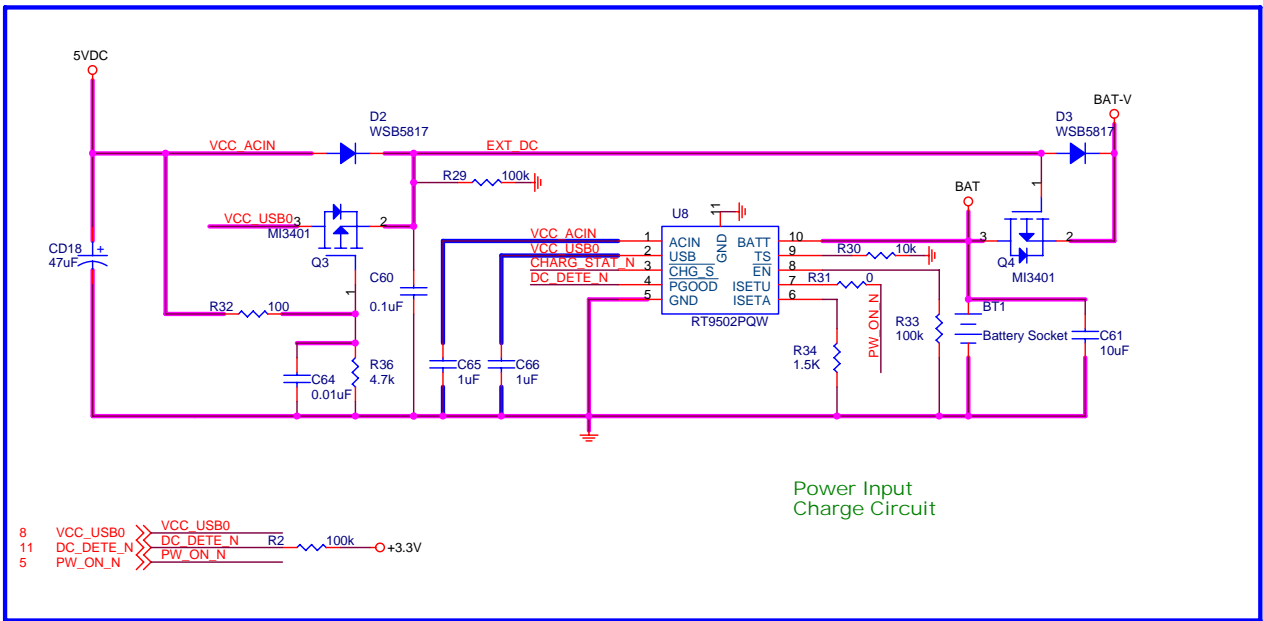
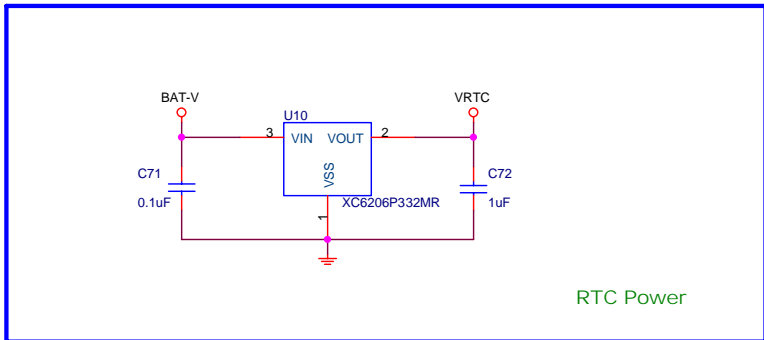
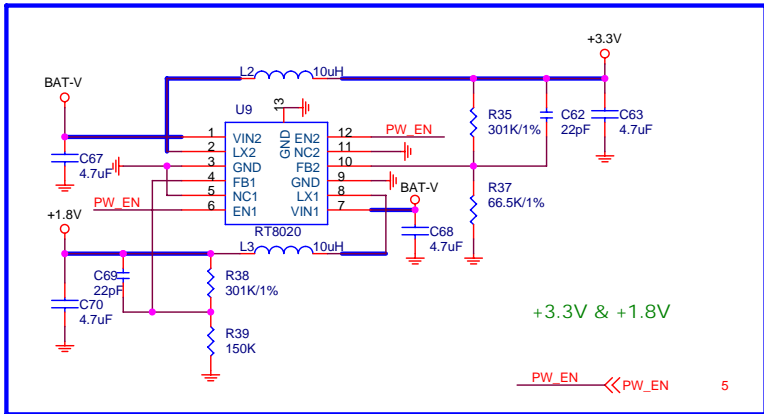
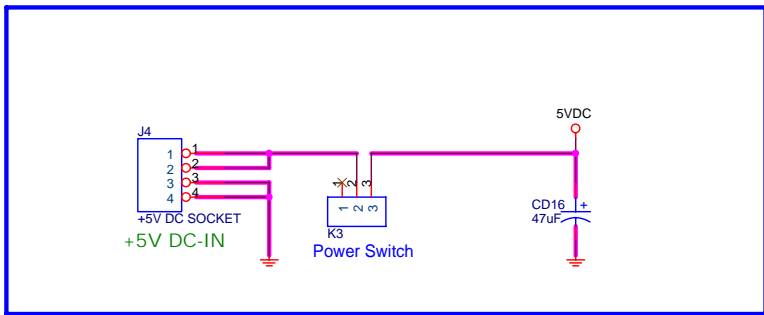


TABLE FOR BOOT MODE SELECT

BOOT_SEL[1:0]	Boot from external ROM/Nor Flash at CS4 or from SPI Nor Flash at SS10	default setting
00	Boot from NAND flash at CS1	
10	Boot from SD Card at MMC0	
11	Boot from USB device	

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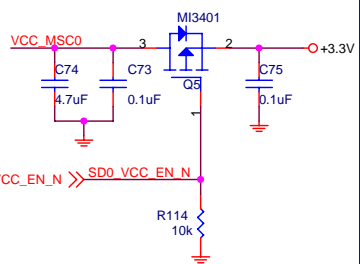
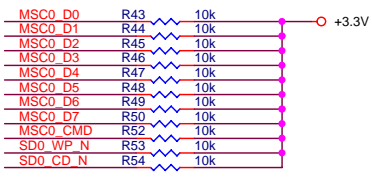
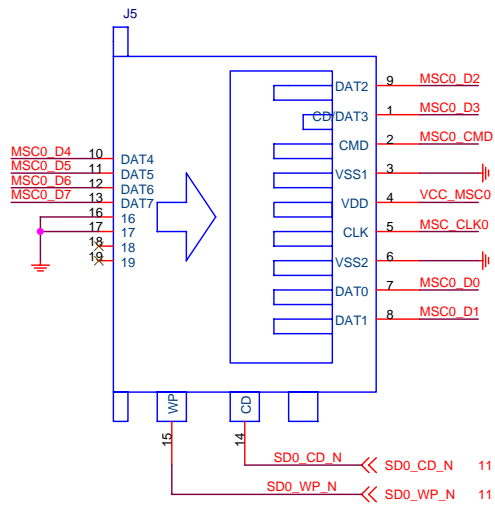
Title RD4750 APUS Board		
Size B	Document Number Boot/Reset/Key	Rev 1.2
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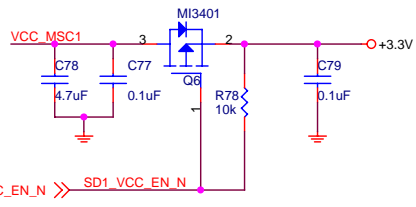
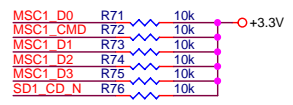
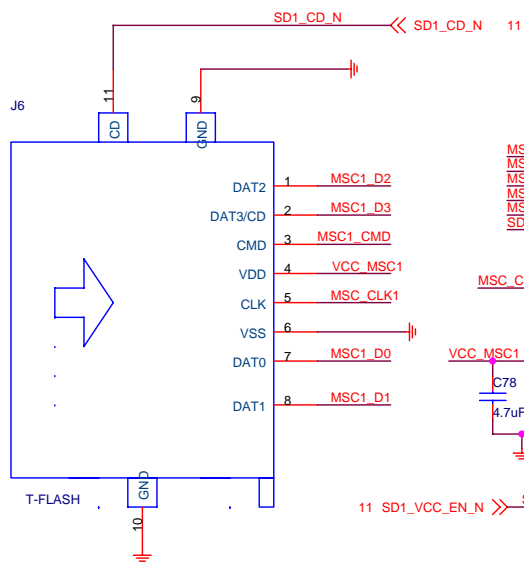
The CHARG_STAT_N net can't be directly connected to GPIO, or there will be leakage after turned off.

40mil 以上 60mil 以上

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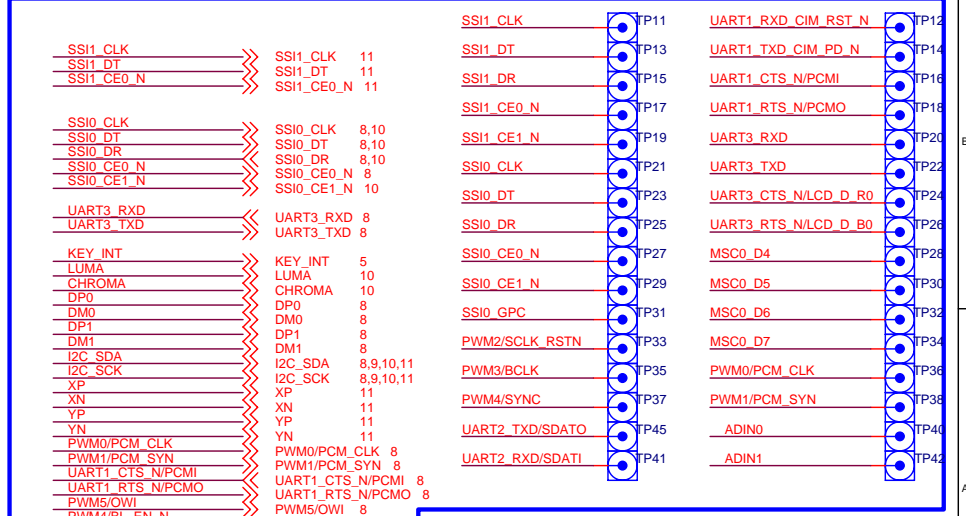
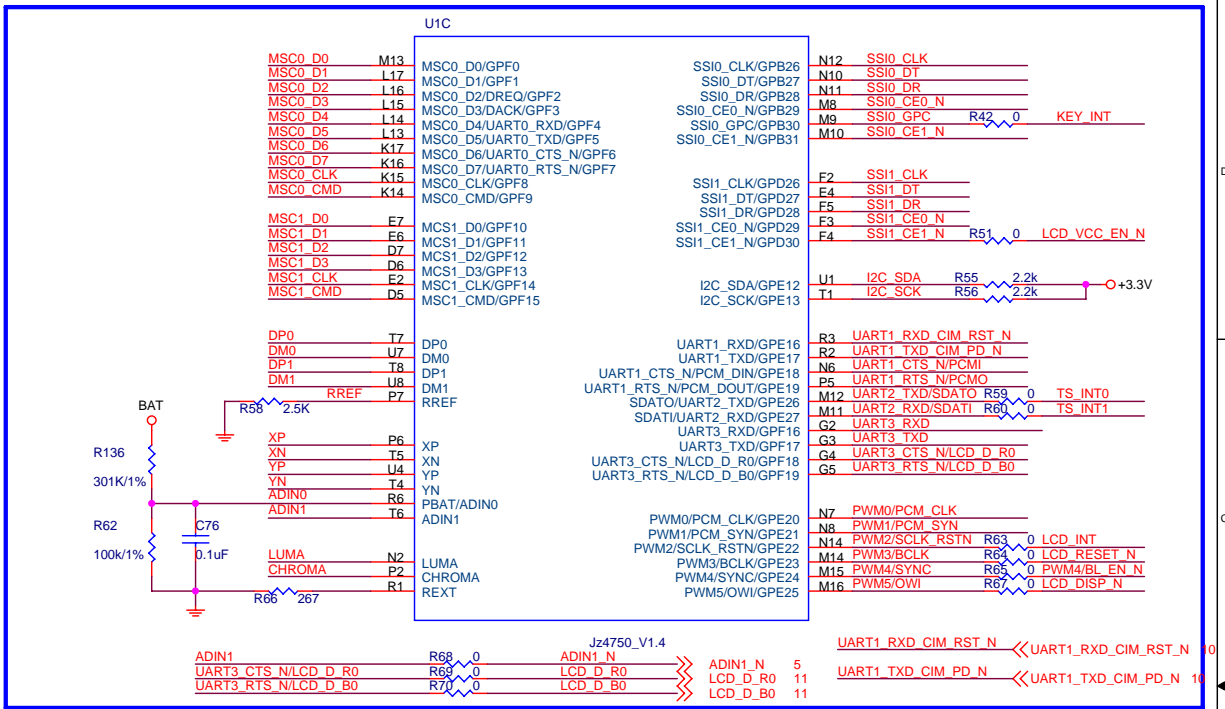


8 Bit MMC/SD Interface



4 Bit TF Interface

Note:MSC1 couldn't be used as BOOT function.

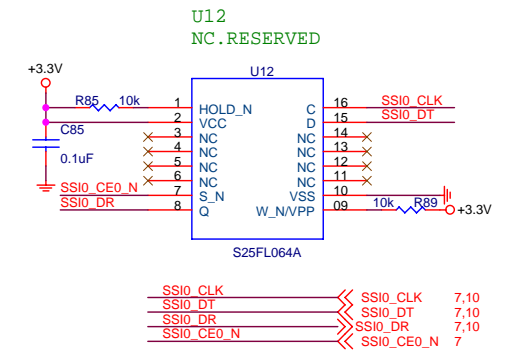
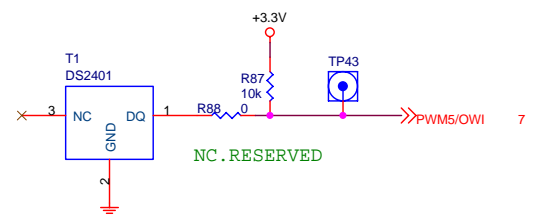
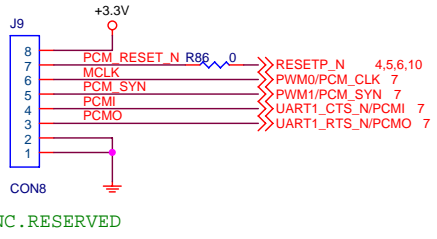
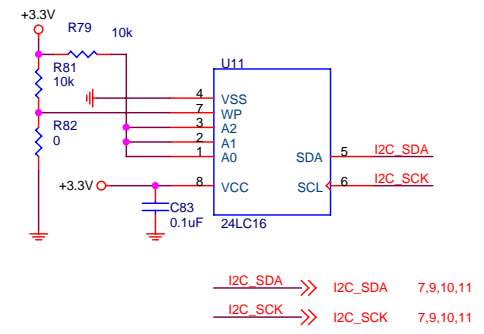
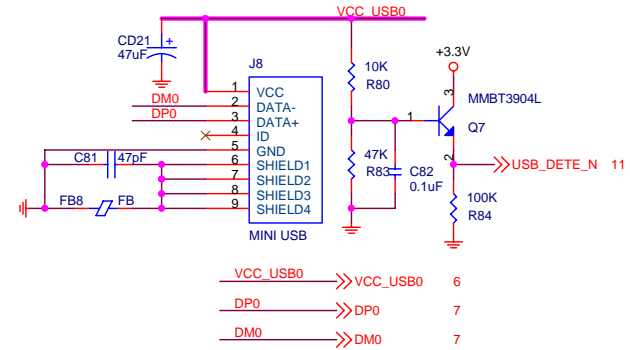
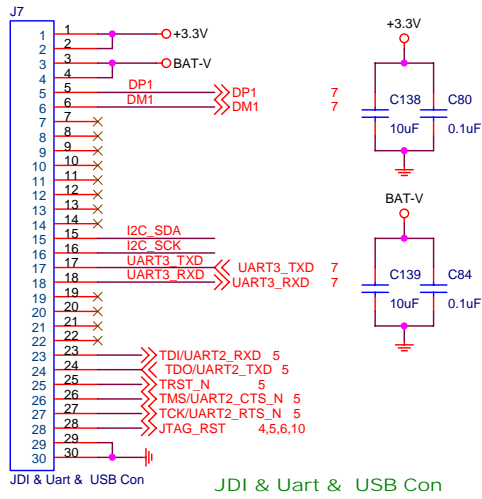


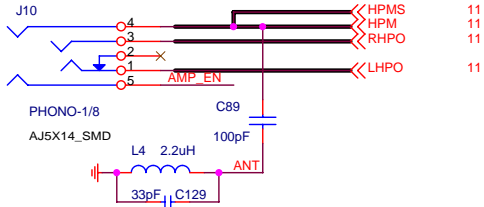
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Title: RD4750 APUS Board

Size: Document Number
B SD/MMC/SPI/I2C Rev: 1.2

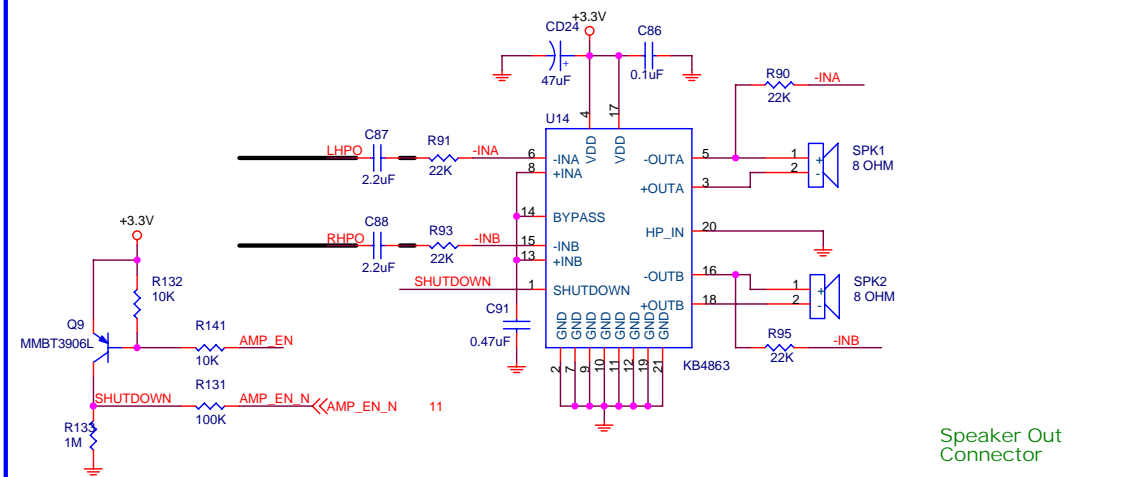
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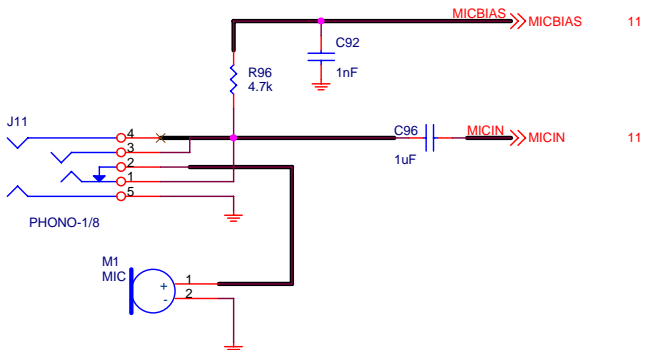


The HPMS and HPM must be connected at terminate, near the headphone Jack

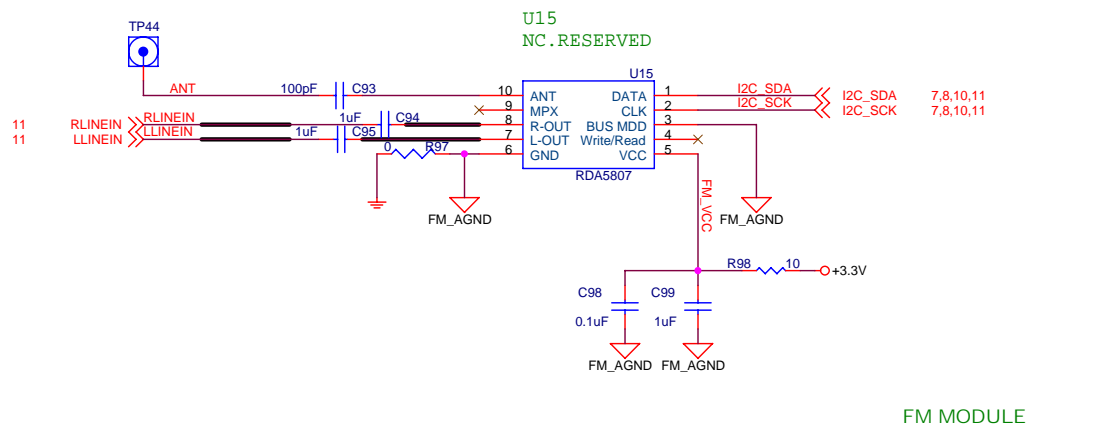
HEADPHONE



Speaker Out Connector



MIC IN

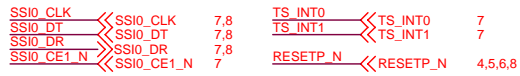
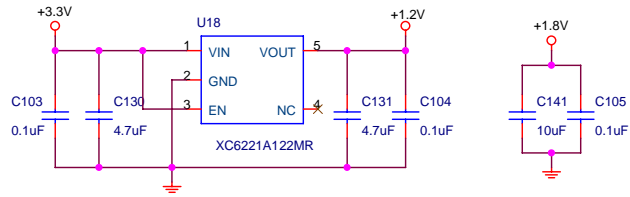
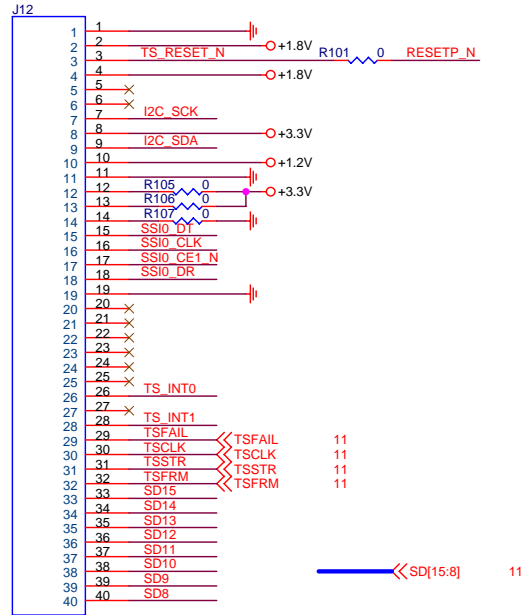


FM MODULE

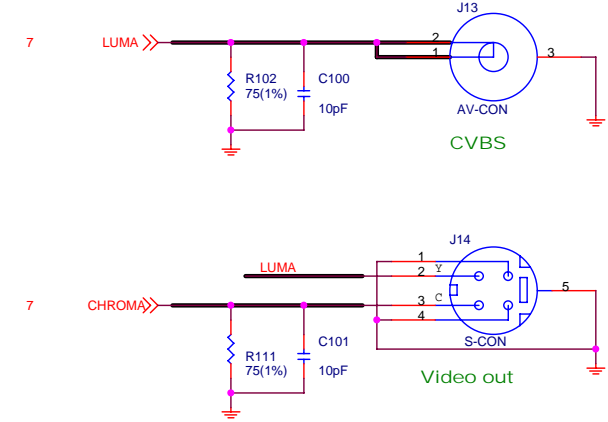
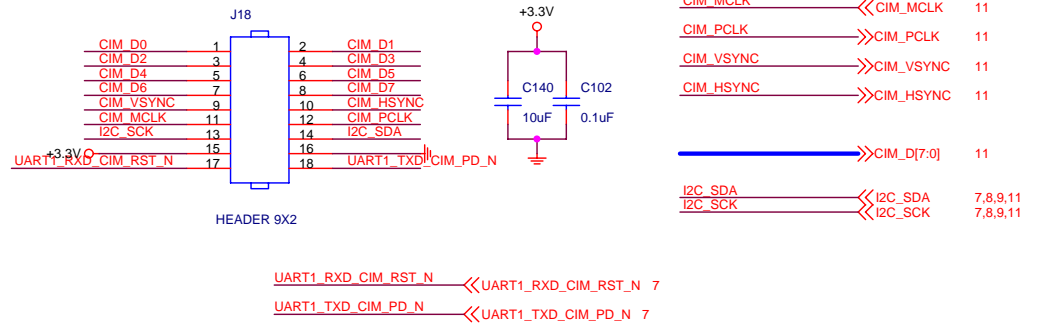
Surround with GND

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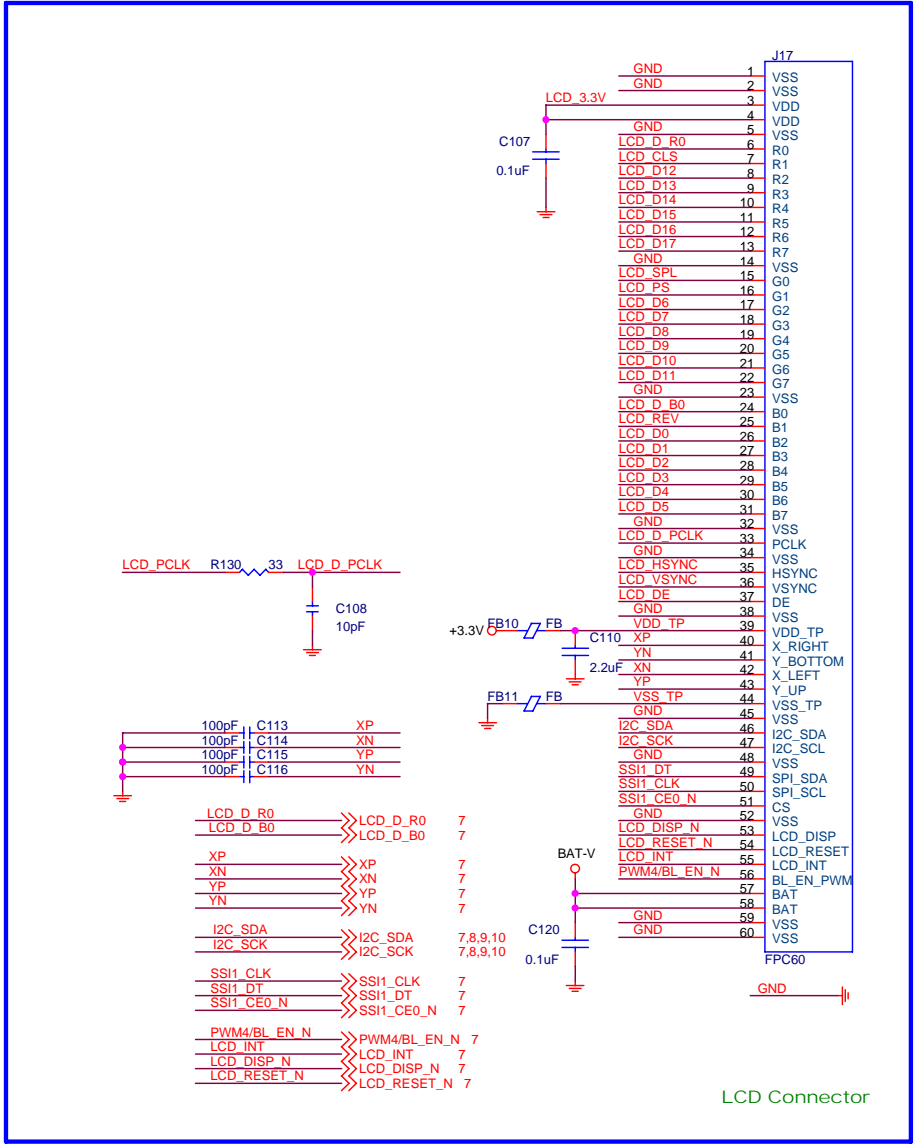
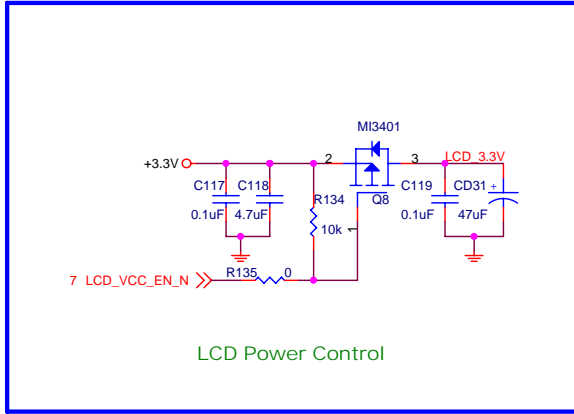
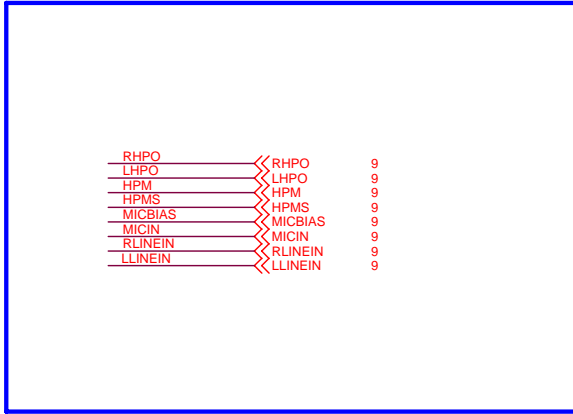
TS Interface Connector



Camera interface Connector



Surround with GND



Data	Revision	Change
Aug. 21 2008	Rev1.0	1. First released Revision
Oct. 13 2008	Rev1.1	1. PAGE07: Add a switch circuit: "cut off BAT ADCIN" to avoid leak current 2. PAGE05 :Change the Jz4750 decouple capacitors 3. PAGE06: Add a battery charge-state LED"D7" indicator circuit 4. PAGE06: Remove Main 5V polyswitch-Type Fuse near K3 5. PAGE09: Add a base current limit resistor for "AMP_EN" BJT transistor Q9 6. PAGE05: Define the default Boot from NAND, boot_SEL [1:0]01. 7. PAGE05: change the Key6 push to active high. 8. PAGE08: Redefine "T1"DS2401 pinout 9. PAGE10: Add two control pins for Camera interface Connector "J18", using GPIO UART1
Dec. 10 2008	Rev1.1.1	1. PAGE07: Change the BAT detect circuit 2. PAGE09: Change R96 from 2.2k to 4.7k 3. PAGE09: Change ANT circuit 4. PAGE05: Change R15 from 0 to 330 Ohm 5. PAGE05: Add Q14,C142,R144. Reserved only for JZ4757 Version A/B 6. PAGE03: Add R145 pullup resistor 7. PAGE04: Add R146 for software busy query
Feb. 11 2009	Rev1.1.2	1. PAGE09: Exchange the places of LHPO and RHPO
Apr. 15 2009	Rev1.1.3	1. PAGE11: Exchange LHPO's and RHPO's PIN numbers in Jz4750 source package. 2. PAGE11: Exchange LLINEIN's and RLINEIN's PIN numbers in Jz4750 source package.
Jun. 1 2009	Rev1.2	1. Just change the revision number for PCB's change.

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